

At a Glance

Digital Leads the Pack with 21164	1
Extending its performance lead, Digital has announced the 21164, a four-way superscalar processor with both first- and second-level cache on chip. The 9.3-million transistor device achieves record performance but will cost as much as \$2,669 at 300 MHz.	
Editorial: What's Happened to the PDA Market?	3
Recent failures such as AT&T's Eo products and the Intel/VLSI partnership demonstrate a lack of momentum for PDAs, but the market will eventually grow as products improve.	
Most Significant Bits	4
3DO chooses PowerPC for next generation; SuperSparc-2, UltraSparc delayed; Analog sinks SHARC price; AMD moves 486DX2 to 80 MHz; Cyrix, Lone Star dispute incompatibilities; Via intros first SLIC chip set; LSI adds Rambus, PCI to ASIC library; Errata: Thunder plans.	
Motorola Announces PowerPC System Logic	11
The MPC105 is the first single-chip complete system-logic solution for PowerPC systems. Motorola and its partners also announced a design kit, reference systems, and tools for Windows NT.	
Improved Cost Model Puts Pentium at \$180	14
The MPR Cost Model has been refined to more accurately model the manufacturing cost of both very large and very small chips. New estimates range from \$1 for the 65C02 to \$180 for Pentium.	
S3 Revamps Graphics Accelerator Line	17
With its first integrated graphics chips, S3 squares off against Cirrus at the low end while extending its performance at the high end.	
Viewpoint: Power Play—Year Two	18
Nick Tredennick explains that while nerds compare the die size of the 601 and Pentium processors, most consumers must be concerned about the long-term viability of the Power Macintosh platform, which may continue to have low volumes in the future.	
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