

# ASP-DAC 2005 Best Papers

## Best Paper Award

**Speed and Voltage Selection for GALs Systems based on Voltage/Frequency Islands**  
*Koushik Niyogi, Diana Marculescu (Carnegie Mellon University)*

**The Polygonal Contraction Heuristic for Rectilinear Steiner Tree Construction**  
*Yin Wang, Xianlong Hong, Tong Jing, Yang Yang (Tsinghua University, Beijing), Xiaodong Hu, Guiying Yan (Institute of Applied Mathematics, Chinese Academy of Sciences)*

**Fast PLL Simulation Using Nonlinear VCO Macromodels for Accurate Prediction of Jitter and Cycle-Slipping due to Loop Non-idealities and Supply Noise**  
*Xiaolue Lai, Yayun Wan, Jaijeet Roychowdhury (University of Minnesota)*

## Design Contest Award

**A Bandwidth Efficient Subsampling-based Block Matching Architecture for Motion Estimation**  
*Hao-Yun Chin, Chao-Chung Cheng, Yu-Kun Lin, Tian-Sheuan Chang (Chiao Tung University)*

**A Low-Power Video Segmentation LSI with Boundary-Active-Only Architecture**  
*Takashi Morimoto, Osamu Kiriya, Hidekazu Adachi, Zhaomin Zhu, Tetsushi Koide, Hans Jürgen Mattausch (Hiroshima University)*