

Workshop Description:

Many of the world's fastest computer systems are PC or workstation clusters. Numerous research groups in academia, industry, and government are currently engaged in cluster research, seeking new ways to advance the state of the art of cluster communication. The goal of the CAC workshop is to bring together researchers and practitioners working in the areas of communication hardware and software to discuss their latest findings as well as future trends in the design of scalable, high-performance, and cost-effective communication architectures for clusters.

Topics of interest include but are not limited to:

- novel network-interface and switch architectures for supporting efficient point-to-point and collective communication,
- design, implementation, and optimization of low-level communication protocols (e.g., VAPI, Tports, GM, and IP) and higher-level communication layers (e.g., MPI, sockets, put/get, and distributed shared memory),
- tools and techniques for evaluating cluster and application performance, and
- communication and architectural issues related to router/switch organization, flow control, congestion control, routing and deadlock handling, load balancing, reliability, QoS support, topology discovery, and dynamic reconfiguration.

Co-Chairs:

Scott Pakin (LANL)
Mazin Yousif (Intel)

Program Committee:

Angelos Bilas (FORTH & U. Crete)
Ron Brightwell (SNL)
Darius Buntinas (ANL)
Wu-Chun Feng (VT)
José Flich (UPV)
Mitchell Gusat (IBM)
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Olav Lysne (Oslo)
Jarek Nieplocha (PNNL)
Greg Pfister (IBM)
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Vikram Saletore (Intel)
Cris Simpson (Intel)
Evan Speight (IBM)
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