EDAA/DATE PhD Forum

The EDAA/DATE PhD Forum offers the opportunity for PhD students to present their thesis work to a broad audience in the design automation and test community from both academia and industry. During the presentation at the DATE Conference it helps students to establish contacts when entering the job market. On the other side, representatives from industry and academia get a glance of state-of-the-art research in design automation and test.

This year we received a total of 60 submissions out of which 46 have been accepted for presentation at a dinner reception. The review process was conducted by a team of 10 internationally renowned reviewers. Our thanks go to all presenters, the PhD Forum Committee and all who were involved in conducting the review process and arranging and organizing the Forum event at DATE. We also thank the EDAA and DATE organizers and representatives for making this Forum possible.

Paul Pop
Chair 2008 EDAA/DATE PhD Forum

PhD Forum Committee

P. Pop (Chair), Technical University of Denmark
R. Buchty, Univ. Karlsruhe, Germany
R. Dick, Northwestern University, USA
R. Dorsch, IBM Böblingen, Germany
F. Gruian, Lund University, Sweden
S. Karlsson, Technical University of Denmark
E. Larsson, Linköping University, Sweden
M. Loghi, Politecnico di Torino, Italy
S. F. Nielsen, Technical University of Denmark
A. Shrivastava, Arizona State University, USA
Y. Xie, Penn. State University, USA

Presentations

Ahmed, W., Curtin University of Technology, “Hardware-Software Partitioning to Address System Evolution and Product Lines in Embedded Systems and SoCs”
Alessandro, C., University of Naples Federico II, “Finite Field Arithmetic for Cryptographic Applications: Theoretical Results, Algorithms and Architectures”
Andriamissaina, C., UBS University, “High-Level Synthesis of Multi-mode Architectures for DSP Applications”
Baloukas, C., Democritus University of Thrace, “Data Structures Optimization Methodology of Dynamic Applications in Embedded Systems”
Bartzas, A., Democritus University of Thrace, “Extraction and Utilization of Software Metadata for Dynamic Embedded Applications”
Beck, A.C.S., Federal University of Rio Grande do Sul, “Transparent Reconfigurable Accelerator for Heterogeneous Behavior Systems”
Brisolara de Brisolara, L., Federal University of Rio Grande do Sul, “Strategies for Embedded Software Development Based on High-level Models”
Brunelli, D., University of Bologna, “Real-time scheduling for Energy Harvesting Sensor Networks”
Cyrille, C., STMicroelectronics, “A Design Space Exploration for Space-Time AdaPtErs”
Gharehbaghi, A.M., Sharif University of Technology, “System-Level Design Verification”
Gioulekas, F., University of Patras, “Architectures and Implementation of Error Correcting Codes”
Hashizume, Y., The University of Kitakyushu, “Post-Silicon Clock-timing Tuning with the Discrete PDE Delay Value”