Programming Shared Memory Multiprocessors with Deterministic Message-Passing Concurrency: Compiling SHIM to Pthreads

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Abstract
Multicore shared-memory architectures are becoming prevalent and bring many programming challenges. Among the biggest are data races: accesses to shared resources that make a program’s behavior depend on scheduling decisions beyond its control. To eliminate such races, the SHIM concurrent programming language adopts deterministic message passing as it sole communication mechanism.

We demonstrate such language restrictions are practical by presenting a SHIM to C-plus-Pthreads compiler that can produce efficient code for shared-memory multiprocessors. We present a parallel JPEG decoder and FFT exhibiting 3.05 and 3.3× speedups on a four-core processor.

1 Introduction
Multicore shared-memory multiprocessors now rule the desktop and server markets, and are poised to dominate embedded systems [9]. While such architectures provide high performance per watt, they present many challenges.

Scheduling—instruction ordering—is the biggest issue in programming shared-memory multiprocessors. While uniprocessors go to extremes to provide a sequential execution model despite caches, pipelines, and out-of-order dispatch units, multiprocessors typically only provide such a guarantee for each core in isolation; instructions are at best partially ordered across core boundaries.

Controlling multiprocessor scheduling is crucial not only for performance, but because data races enable scheduling to affect a program’s functional behavior. Worse, the operating system schedules nondeterministically.

Many tools attempt to detect races in concurrent programs with shared memory. The SPIN model checker [7] simulates all execution orders of a concurrent program and looks for inconsistencies, Eraser [13] looks for consistent locking behavior, and Atomizer [5] checks atomicity. But these have limitations: SPIN only works on small models; Eraser and Atomizer are testing-based and can miss bugs.

As an alternative, the SHIM language [3, 15] only allows deterministic message-passing communication to guarantee race freedom. The programming model allows SHIM compilers to use a simple syntactic check to verify that runtime

scheduling choices cannot change a program’s I/O behavior. While this model does restrict how concurrent tasks may interact, the burden for the programmer and the performance penalty are a small price for correctness.

Here, we demonstrate SHIM facilitates writing interesting, efficient parallel programs for shared-memory multiprocessors. We implement a parallel JPEG decoder and an FFT to show how SHIM helps with coding and testing different schedules during design exploration (Sec. 3). We present a compiler that generates C code that calls the POSIX thread (“Pthread”) library for shared-memory multiprocessors (Sec. 4). For the JPEG and FFT examples, our compiler’s output achieves 3.05 and 3.3× speedups on a four-core processor (Sec. 5).

2 The SHIM model and language
SHIM [15] is a concurrent programming language designed to guarantee scheduling independence. The input/output function of a SHIM program does not depend on scheduling choices, i.e., if two concurrent tasks are ready to run, choosing one does not affect the program’s function.

It adopts an asynchronous concurrency model, à la Kahn networks [8] (SHIM tasks can only block on a single channel), that uses CSP-like rendezvous [6]. Only communication affects the relative execution rates of concurrent tasks.

The language does not expose shared memory to the programmer, but it does provide single-sender multiple-receiver synchronous communication channels and asynchronous exceptions. Both mechanisms were designed to prevent scheduling decisions from affecting function.

SHIM’s syntax is a C subset augmented with constructs for concurrency, communication, and exceptions. It has

```c
void h(chan int &A) {  
 A = 4; send A;  
 A = 2; send A;  
}

void f(chan int A) throws Done {  
 recv A;  
 throw Done;  
}

void g(chan int A) {  
 recv A;  
}

main() {  
 try {  
 chan int A;  
 f(A); par g(A);  
 } catch (Done) {}  
}
```

Fig. 1. A concurrent SHIM program with communication and exceptions

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functions with by-value and by-reference arguments, but no global variables, pointers, or recursive types.

The `par` construct starts concurrent tasks. `p par q` starts statements `p` and `q` in parallel, waits for both to complete, then runs the next statement in sequence.

To prevent data races, SHIM forbids a variable to be passed by reference to two concurrent tasks. For example,

```c
void f(int i, x) () { void g(int x) {} int x, y; f(x); par g(x); par f(y); // OK f(x); par f(x); // rejected because x is passed by reference twice }
```

Internally, our compiler only supports parallel function calls. If `p` in `p par q` is not a function call, `p` is transformed into a function whose interface—the formal arguments and whether they are by-reference or by-value—is inferred [15].

SHIM’s channels enable concurrent tasks to synchronize and communicate without races. The `main` function in Fig. 1 declares the integer channel `A` and passes it to `f` and `g`, then `f` passes it to `h` and `j`. Tasks `f` and `h` send data with `send A`. Tasks `g` and `j` receive it with `recv A`.

A channel resembles a local variable. Passing a channel by value copies its value, which can be modified independently. A channel must be passed by reference to senders.

Communication is blocking: a task that attempts to communicate must wait for all other connected tasks to engage in the communication. If the synchronization completes, the sender’s value is broadcast to the receivers. In Fig. 1, `A` is broadcast from `h` to `g` and `j`. Task `g` blocks on the second `send A` because task `f` does not run a matching `recv A`.

Like most formalisms with blocking communication, SHIM programs may deadlock. But deadlocks are easier to fix in SHIM because they are deterministic: on the same input, a SHIM program will either always or never deadlock.

SHIM’s exceptions enable a task to gracefully interrupt its concurrently running siblings. A sibling is “poisoned” by an exception when it attempts to communicate with a task that raised an exception or with a poisoned task. For example, when `j` in Fig. 1 throws `Done`, it interrupts `h`’s blocked `send A` and `g`’s blocked `recv A`. An exception handler runs after all the tasks in its scope have terminated or been poisoned.

### 3 Design exploration with SHIM

SHIM facilitates the coding and testing of different schedules—a key aspect of design exploration for parallel systems. To illustrate, we describe implementing two parallel algorithms in SHIM: a JPEG decoder and an FFT.

#### 3.1 Porting and parallelizing a JPEG decoder

We started by porting into SHIM a sequential JPEG decoder written in C by Pierre Guerrier. SHIM is not a C subset, so some issues arose. The C code held Huffman tables in global variables, which SHIM does not support, so we passed the tables explicitly. It allocated buffers with malloc; we used fixed-size arrays. We discarded a pointer-based Huffman decoder, preferring instead one that used arrays.

After some preprocessing, the main loop of the original program unpacked a macroblock—six Huffman-encoded 8×8 data blocks (standard 4:2:0 downsampling)—performed an IDCT on each data block, converted from YUV to RGB, and blitted the resulting 16×16 pixel block to a framebuffer. It then wrote the framebuffer to a file. Although macroblocks can be processed independently, unpacking and writing are sequential (Fig. 2).

We first ran four IDCT transformers in parallel. Unfortunately, this ran slowly because of synchronization overhead.

To reduce overhead, our next version divided the image into four stripes and processed each independently. Fearing the cost of communication, we devised the seven-task schedule in Fig. 3, which greatly reduced the number of synchronizations at the cost of buffer memory.

```c
void unpack(unpacker_state &state, stripe &stripe) {
    void process(const stripe &stripe, pixels &pixels) {
        void write(writer_state &state, pixels &pixels) {
            unpacker_state ustate; writer_state wstate; stripe stripe1, stripe2, stripe3, stripe4;
            pixels pixels1; chan pixels pixels2, pixels3, pixels4;
            unpack(ustate, stripe1);
            (process(stripe1, pixels1); write(wstate, pixels1); recv pixels2; write(wstate, pixels2);
            recv pixels3; write(wstate, pixels3); recv pixels4; write(wstate, pixels4);
            par {
                unpack(ustate, stripe2);
                (process(stripe2, pixels2); send pixels2;
                par {
                    unpack(ustate, stripe3);
                    (process(stripe3, pixels3); send pixels3;
                    par {
                        unpack(ustate, stripe4);
                        (process(stripe4, pixels4); send pixels4;
                    } })
                }
            }
        }
    }
}
```
3.3 Race freedom

Both the JPEG and FFT examples illustrate that dividing and scheduling computation tasks is critical in achieving performance on parallel hardware. Although data dependencies in JPEG were straightforward, finding the right schedule took some effort. With traditional concurrent formalisms, it is easy to introduce data races during design exploration.

SHIM’s channels and exceptions cannot introduce races. E.g., in Fig. 6, the first task throws an exception after reading all the rows. SHIM semantics ensure that the three row-processing tasks and the writing task terminate just after they have completed processing all the rows.

SHIM also guarantees data dependencies are respected. For instance, the SHIM compiler rejects attempts to run unpackers in parallel because of the shared pass-by-reference state (mostly, position in the file):

void unpack(unpacker_state &state, row &row1) {...}
void process(in row row, out pixels pixels)
{ for (;;) { recv row; // IDCT etc. of send pixels; } }
void write(writer_state wstate, const pixels &pixels) {...}
unpacker_state ustate; writer_state wstate; int rows;
chan row row1, row2, row3;
chan pixels pixels1, pixels2, pixels3;
try {
  (for (;;) {
    unpack(ustate, row1); send row1; if (--rows == 0) break;
    unpack(ustate, row2); send row2; if (--rows == 0) break;
    unpack(ustate, row3); send row3; if (--rows == 0) break;
  } throw Done; ) par
process(row1, pixels1); par
process(row2, pixels2); par
process(row3, pixels3); par
  (for (;;) { recv pixels1; write(wstate, pixels1);
    recv pixels2; write(wstate, pixels2);
    recv pixels3; write(wstate, pixels3); } )
catch (Done) {} }

Fig. 6. SHIM code for the JPEG schedule in Fig. 5

The Fig. 3 schedule only gave a 1.8× speedup because the seventh task waits for all the other stripes to be unpacked and then everything waits for the seventh task. The arrow in Fig. 3 shows the critical path, which includes the total cost of Huffman decoding and 1/4 of the DCTs.

To strike a balance between the two approaches, we finally settled on the more fined-grained schedule in Fig. 5. Each task processes a row of macroblocks at a time (e.g., 64 macroblocks for a 1024-pixel-wide image). This schedule spends less time waiting than the stripe-based approach and synchronizes less often than the block-based approach.

3.2 Parallelizing an FFT

We also coded in SHIM a pipelined FFT to test the effects of numerical roundoff. Its core is the FFT from Numerical Recipes [11], which we rewrote to use signed 4.28 fixed-point arithmetic. We added code that parses a .wav file, runs blocks of 1024 16-bit samples through the FFT, through an inverse FFT, then writes the samples to another .wav file.

Our FFT uses a schedule similar to that of the more complex JPEG decoder: one task reads 1024-sample blocks and feeds them to four FFT tasks in a round-robin manner. Each reads its sample block, performs the FFT/inverse FFT operation, and sends its block to a writer task, which receives sample blocks in order and writes them sequentially.

Synchronization costs limited this to a 2.3× speedup on four processors, so we made it process 16 1024-sample blocks, improving performance to 3.3×.
We encode in a bit vector the subtree of functions connected to a channel. Since we know at compile time which functions can connect to each channel, we assign a unique bit to each function on a channel. We check these bits at run time with logical mask operations. In the code, something like $A_f$ is a constant that holds the bit our compiler assigns to function $f$ connected to channel $A$, such as $0x4$.

### 4.3 Implementing rendezvous communication

Implementing SHIM’s multiway rendezvous communication with exceptions is the main code generation challenge.

The code at a send or receive is straightforward: it locks a channel and waits or blocks on the channel.

For each channel, our compiler generates an event function that manages communication. Our code calls an event function when the state of a channel changes, such as when a task blocks or connects to a channel.

Fig. 9 shows the event function our compiler generates for channel $A$ in Fig. 1. While complex, the common case is quick: when the channel is not ready (one connected task is not blocked on the channel) and no task is poisoned, $A_{\text{connected}} \neq A_{\text{blocked}}$ and $A_{\text{poisoned}} = 0$ so the bodies of the two if statements are skipped.

If the channel is ready to communicate, $A_{\text{blocked}} = A_{\text{connected}}$ so the body of the first if runs. This clears the channel (blocked = 0) and main’s value for $A$ (passed by reference to $f$ and $h$) is copied to $g$ or $j$ if connected.

If at least one task connected to the channel has been poisoned, $A_{\text{poisoned}} = 0$ so the body of the second if runs.

### 4.4 Starting and terminating tasks

It is costly to create and destroy a POSIX thread because it usually requires a system call, each has a separate stack, and doing so interacts with the operating system’s scheduler. To minimize this overhead, because we know the call graph of the program at compile time, our compiler generates code that creates at the beginning as many threads as the SHIM program will ever need. These threads are only destroyed when the SHIM program terminates; if a SHIM task terminates, its POSIX thread blocks until it is reawakened.
lock(A.mutex); /* connect */
A.connected |= (A.f,A.g); event(A);
unlock(A.mutex);
lock(main.mutex);
main.attached_children = 2;
unlock(main.mutex);
lock(f.mutex); /* pass args */
f = & A;
unlock(f.mutex);

/* A is dead on entry for g, so do not pass A to g */

4.5 Optimizations

SHIM draws no distinction between sequential C-like functions and concurrent tasks; our compiler treats them differently for efficiency. Our compiler distinguishes tasks from functions, which must not take any channel arguments, contain local channels, throw or handle exceptions, have parallel calls, call any tasks, or be called in parallel. Tasks are implemented as described above—each is assigned its own thread. Functions follow C’s calling conventions.

Unlike Java, SHIM passes scalars, structures, and arrays by value unless marked as by-reference. This is convenient at parallel call sites to avoid interference among concurrent tasks. However, if tasks only read some data, the data can be shared among them for efficiency. Similarly, a channel can be shared among tasks that never update the channel’s value between recv instructions. We introduced a C++-like const specifier that prohibits assignments to a variable, channel, or function parameter. The compiler allows multiple concurrent const by-reference parameters and allocates a shared copy for const parameters passed by value.

We implemented another optimization to reduce superfluous copies of large data structures. Normally, the current value of a channel is copied when the channel is passed by value, but copying is unnecessary if the value is never used before the next value is recv’d. The overhead can be substantial for arrays. We perform live variable analysis to determine which arguments are dead on entry. E.g., in

```c
void myfunc(chan int input[65536]) { recv input; ... }
```

the input channel value is dead on entry and will not be copied at any call site for myfunc, eliminating a 256K copy.

5 Experimental results

We implemented our SHIM compiler in OCAML. Code specific to the Phthreads backend is only about 2000 lines.

To test the performance of our generated code, we ran it on a 1.6 GHz Quad-Core Intel Xeon (E5310) server running Linux kernel 2.6.20 with SMP (Fedora Core 6). The processor “chip” actually consists of two dice, each containing a pair of processor cores. Each core has a 32 KB L1 instruction and a 32 KB L1 data cache, and each die has a 4 MB of shared L2 cache shared between the two cores.

We compiled the generated C with gcc 4.1.1 with -O7 and -pthread options. We timed it using the time command and ran sync to flush the disk cache. We used Dag Wieers’s run command¹ to limit the number of available cores. The JPEG program uses much more stack space than typical C programs because it stores all data on the stack instead of the heap. We raised the stack size to 16 MB with ulimit -s.

Table 1 shows results for the JPEG decoder. We ran it on a 20 MB earth image from NASA² and varied both the number of available processors and the number of row-processing

¹www.ccur.com/oss
²world.200409.3x21600x10800.jpg from earthobservatory.nasa.gov
The speedup due to parallelization plateaued at 3.05, which we attribute to the sequential nature of the Huffman decoding process.

Table 2 shows statistics for our FFT. We compared handwritten C with sequential SHIM and two parallel SHIM versions, one with six tasks that work on single 1024-sample blocks and one that works on sixteen such blocks. The first parallel implementation has overhead from synchronization and communication. The “Parallel 16” version communicates less to reduce this overhead and achieve a 3.3× speedup: 82% of an ideal 4× speedup on four cores.

6 Related work

Like SHIM, the StreamIt language [16] is deterministic, but its dataflow model is a strict subset of SHIM’s and there is no StreamIt compiler for shared memory machines.

Other concurrent languages use different models. The most common is “loops-over-arrays,” embodied, e.g., in compilers for OpenMP [17]. This would be awkward for a schedule such as Fig. 5. The Cilk language [2] speculates to parallelize sequential code. The Guava [1] Java dialect prevents unsynchronized access to shared objects by enforcing monitor use with a type system. Like SHIM, it aims for race freedom, but uses a very different model.

7 Conclusions

A good parallel algorithm reliably computes the result quickly. Unlike most parallel languages, SHIM guarantees reliability by preventing data races. Correctness remains a challenge, but at least running a SHIM program on a test case gives consistent results for any scheduling policy.

SHIM is helpful during design exploration when testing different schedules; its determinacy makes it easy to obey data dependencies. Its C-like syntax facilitates porting existing code. We demonstrated this on a JPEG decoder.

Our SHIM compiler generated code for parallel programs that runs on a four-core processor over three times faster than sequential C. Sequential SHIM code runs no slower.

Our plans for SHIM include formal verification, code generation fusing parallelism with static scheduling [4], extracting parallelism [10], data distribution [14], communication optimization [12], and the synthesis of hardware.