MONDAY PANEL:

SEMICONDUCTOR SLOWDOWN: WHO WILL BLINK FIRST?

Moderator: John Darringer - IBM Corp., TJ Watson Research Ctr., Yorktown Heights, NY

The entire electronics industry has benefited from decades of remarkable adherence to "Moore's Law". Is the fantastic ride over? Is this predicted slowdown for real? What will be the limiting factor? Will the technologists finally fail to deliver the sub-100nm factories on time? Will design automation tools be able to deliver completed designs with the needed productivity advances? Or will the designers finally fail to find an economically viable use for a billion transistors on a chip?

To debate these topics we have a distinguished panel of experts from academia and industry representing technology, tools and design. Come prepared with your questions for the open discussion.

Panelists:

Mark Horowitz - Stanford Univ., Stanford, CA
Yoshiharu Furui - Sony Semiconductor Kyushu Corp., Isahaya-shi, Nagasaki-ken Japan
Kerry Bernstein - IBM Corp., TJ Watson Research Ctr., Yorktown Heights, NY
Ivo Bolsens - Xilinx, Inc., San Jose, CA
Joseph Sawicki - Mentor Graphics Corp., Wilsonville, OR
Aki Fujimura - Cadence Design Systems, Inc., San Jose, CA
Bill Grundmann - Intel, Shrewsbury, MA