Panel: **The John Henry Syndrome:**  
*Humans vs. Machines as FPGA Designers*

Moderator: Herman Schmit, Carnegie Mellon University

Panelists: Ray Andraka, Andraka Consulting  
Philip Friedin, GuideTech, Inc.  
Satnam Singh, Xilinx Corporation  
Tim Southgate, Altera Corporation  
(Additional panelists to be announced)

Human designers have done amazing things with FPGAs. These designs challenge our assumptions about the speeds and densities achievable by programmable hardware. But with multi-million gate designs and increasingly complex FPGA architectures, is there really any place for the hand-crafted design anymore? Is there a way that CAD tools can incorporate the techniques and knowledge of designers to create high-density, high-performance implementations automatically? Or will the tools and architectures always lag the applications, thereby guaranteeing abundant job opportunities for FPGA design experts?