Message from the Workshop Chairs

Hardware/software codesign has become a strategic technique for design of modern electronic systems because it has the potential to bridge the increasing gap between silicon capacity and designer productivity. On the one hand, today’s silicon capacity allows for very complex systems, consisting of a mixture of multiple software and hardware processors including peripheral devices, to be implemented on a single chip (System-On-Chip). On the other hand, many applications have relatively short time-to-market, demanding fast development where numerous parameters such as performance, power consumption, flexibility, reliability, production cost, etc. have to be captured and explored in order to produce a successful product in time. The CODES workshop represents the major international forum for presentation of the latest research and practice in codesign.

This year we had 61 submissions from 13 countries. All submissions went through a thorough review process, resulting in 29 papers accepted. Of these, 20 papers were accepted for regular presentation and 9 papers for short presentation. During the workshop each paper is presented as a (regular or short) talk, followed by an interactive poster session. In addition, the workshop program includes two invited talks from industry.

As the major international forum for research in codesign, the workshop reflects the current trend in codesign research and practice. This year’s technical program represents several important topics:

- Estimation and optimization techniques for a variety of system quality metrics, such as performance, power consumption and memory issues;
- Issues on IP core design and reuse;
- System validation and simulation;
- Embedded software optimization and generation;
- And the continuing interest in system level modeling and integration, which has been an important issue from the very beginning of this series of workshops.

As last year, the proceedings will be available as hardcopy, on CD-ROM on the ACM SIGDA annual compendium, and on the ACM SIGDA web page

http://www.sigda.acm.org/Archives/ProceedingArchives/.

We are grateful for the SIGDA’s forward-looking publication policy, and their accommodating an accelerated schedule enabling the submission deadline to occur only four months before the meeting date. This accelerated schedule was also made possible by the entire submission, review, and final camera-ready submission processes occurring completely electronically via email and the web.

We wish to thank the Technical Program Committee for their efforts in reviewing papers and for contributing to a thorough selection process. We would also like to thank Peter Voigt Knudsen for developing a review process tool that was a tremendous help during the selection process.

Further information on the workshop, online proceedings, and past and future CODES workshops is at:

http://www.it.dtu.dk/codes2000

Welcome to CODES 2000

Jan Madsen
Program Chair

Frank Vahid
General Chair