Moderator: Rolf Ernst - Technical Univ. of Braunschweig, Braunschweig, Germany

There will be a new edition of the SIA roadmap this year. The SIA roadmap has been used by the semiconductor industry to develop new products and technologies in close synchronization. The SIA roadmap also contains a CAD section which defines the tools necessary to make the predicted technological progress happen.

In general, roadmaps are developed to support management decisions and to control longer term technological innovation, and - in this context - define relevant fields for academic research. Despite a much smaller size, the EDA industry experiences similar delay times from academic research to widely accepted products. Complex interdependencies between CAD tools, IP libraries, standardization processes and company design methodologies make predictions difficult. Embedded system design added additional hooks to the world of software development. So, reliable CAD roadmaps could be a valuable decision support for the EDA, semiconductor and systems industries.

On the other hand, roadmap prophecies, if taken serious, tend to be self-fulfilling due to their influence on technological investments and, eventually, on research funds. So, roadmaps could potentially stifle innovation especially in a highly dynamic area such as system design.

The distinguished panel will discuss the usefulness and the impact of CAD roadmaps. It includes authors of the SIA roadmap, of Asian and European CAD roadmaps, as well as highly influential representatives from academia and from the EDA industry.

Panel Members:

Ivo Bolsens - IMEC, Leuven, Belgium
Raul Camposano - Synopsys, Inc., Mountain View, CA
Tamotsu Hiwatashi - Toshiba Corp., Kawasaki, Japan
William Joyner - SRC, Research Triangle Park, NC
Edward A. Lee - Univ. of California, Berkeley, CA
Richard Newton - Univ. of California, Berkeley, CA
Gabriele Saucier - IMAG, Grenoble, France