EDA to the Rescue of System on a Chip

Silicon capacity has doubled every 18 months and will continue to do so for some time. While spiraling gate counts allow designers to pack more and more functions onto a single piece of silicon, they also highlight our inability to keep up with such staggering complexity. The good news is that EDA technology will be the enabler for designers who want to take advantage of this increasing silicon capacity. This presentation will outline the specific challenges facing the EDA industry and its customers in the coming years - as well as predictions for the solutions that will be adopted.

Dr. Aart J. de Geus is Chairman and CEO of Synopsys, Inc. Since co-founding Synopsys in 1986, Dr. de Geus has grown the company from a start-up focused on breakthrough synthesis technology to an international EDA market leader with over 2,900 employees. Today, Synopsys is focused on delivering a complete line of best-in-class EDA software, technologies, intellectual property reuse, and consulting services necessary to succeed in the era of deep submicron and system-on-a-chip design.

Dr. de Geus is considered one of the leading experts worldwide in electronic design automation and frequently speaks on the direction of the EDA industry and its technology. In April 1998, Dr. de Geus was elected Chairman of the Board of the EDA Consortium. Dr. de Geus has also served on the Board of Governors of the IEEE Circuits and Systems Group and as Chairman of the Computer and Network Design (CANDE) group. For his many achievements in the field of electronics, Dr. de Geus was made a Fellow of the Institute of Electrical and Electronics Engineers in January 1999. He holds an M.S.E.E. from the Swiss Federal Polytechnical Institute and a Ph.D. in electrical engineering from Southern Methodist University.