Design and EDA Roadmap

Greg Ledenbach
SEMA TECH
2706 Montopolis Dr, Austin, TX 78717-6499

The EDA Industry Council, a global adhoc group of EDA, design industry, and electronics design standards representatives, has initiated an effort to revise "The EDA Standards Roadmap for Design and Test". This roadmap identifies the current status, near and long range needs for both electronics design and EDA standards needed for associated EDA design technology. The current edition this EDA standards roadmap was released in late 1994 and is available from "http://www.si2.org/roadmap"

Recently, the SIA revised its National Technology Roadmap for Semiconductors (NTRS-97) which contains sections on near and long range needs for integrated circuit design and test technology. The goal of the revision of the EDA Standards Roadmap for Design and Test is to, in part, align its status, planning, and design technology standards forecasts with the NTRS-97. The revision effort is also focused on more completely cover the EDA needs of all electronics design and design technology.

This panel will cover the needs, overall plan, and global initiatives of updating this important EDA roadmap. An important aspect of this panel is that the audience should listen and take note of how they can participate, contribute, and take responsibility for accomplishing this critical roadmap update.

The panel is composed of key Industry Council and associated roadmap involved industry representatives who are already involved with both the NTRS-97 update and with updating the EDA Standards Roadmap. The panel will share their personal and technical views on key roadmap design and test needs, and their overall plans, and global industry initiatives for updating this important EDA roadmap.

Specifically, the panel members will make presentations on overall electronics EDA design and design technology needs, including those stemming from the NTRS-97 Design and Test update, and specific plans and organization to be used to carry out the EDA Roadmap update. Presentations will then be made on the impact, interests, and participation that is anticipated in Europe, Japan and the fareast.

Organizer;
Steve Grout (SEMATECH, USA)

Moderators;
Greg Ledenbach (SEMATECH, USA)
Steve Grout (SEMATECH, USA)

Panel members;
Steve Schulz (Texas Instruments, USA)
— Overall electronics EDA needs

Don Cottrell (Silicon Integration Initiative, USA)
— EDA Roadmap Update Plan and Organization

Jean Mermet (European CAD Standardization Initiative, France)
— Europe interests and participation

Tamotsu Hiwatashi (Toshiba Corp. and EIAJ, Japan)
— Japan interests and participation