Foreword

Welcome to Dana Point for the 1995 International Symposium on Low Power Design (ISLPD-95).

ISLPD-95 is a premier symposium devoted solely to the theory and practice of low power VLSI design. This symposium brings together the technical contributors in all areas of design and test of low power electronic circuits and systems.

To allow attendees the opportunity to listen to all the papers, the Symposium has no parallel sessions. In addition to regular papers that are presented in the sessions, we have accepted a number of short papers that will be presented as posters in the Symposium.

There are two panel discussions and a keynote speech. The first panel debates a number of important and controversial topics in low power while the second panel is organized as a series of short invited talks presenting the current state-of-the-art in power consumption of different types of circuits. We are especially pleased to have Walter Davis as the Keynote Speaker. He brings his unique experience in the development of portable communications equipment and VLSI circuits for communications applications and broad perspective on the low power design opportunities and challenges.

Thanks are due to the program committee for doing an excellent job in very difficult circumstances to put together a strong program, and to the panel organizers and panelists for what should prove to be interesting sessions.

Finally, we thank Paul Weil and Debbie Hall from the ACM SIGDA and Phillip Allen from the IEEE CAS for their sponsorship and unqualified support. We would also like to thank Cheng-Tah Hsieh and Kuo-Rueih Pan of USC who spent many hours in producing the symposium proceedings.

We hope that you will find the Symposium exciting and useful. We welcome your comments and suggestions on the Symposium.