Session 10B: Modeling and Design of ASIPs

Moderators:

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This Session is concerned with the design and use of application-specific instruction-set processors (ASIPs). The first paper discusses high-level metrics for software running on programmable processors. The second paper introduces a processor model. The third paper presents a methodology for ASIP design.

Software Estimation Using a Generic-Processor Model
J. Gong, D.D. Gajski, and S. Narayan

Describing Instruction Set Processors Using nML
A. Fauth, J. Van Praet, and M. Freericks J.-P. Dauvin, SGS-Thomson Microelectronics, France

Incorporating Compiler Feedback Into The Design of ASIPs
F. Onion, A. Nicolau, and N. Dutt