Session 7A: Applications of Symbolic Traversal Techniques
Moderators: R. Kumar, FZI Karlsruhe, Germany, and N. Fristacky, Slovak Technical University, Bratislava, Slovakia

Recent applications of BDD-based symbolic traversal techniques to MOS-circuits, graph algorithms, asynchronous circuits, process calculi are presented.

Verifying Real-Time Properties of MOS-Transistor Circuits
  J. Frossl and Th. Kropf

Using Symbolic Techniques to Find the Maximum Clique in Very Large Sparse Graphs
  F. Corno, P. Prinetto, and M. Sonza Reorda

Checking Signal Transition Graph Implementability by Symbolic BDD Traversal
  A. Kondratyev, J. Cortadella, E. Pastor, O. Roig, M. Kishinevsky, and A. Yakovlev

Proving Testing Preorders for Process Algebra Descriptions
  F. Corno, M. Cusinato, M. Ferrero, and P. Prinetto