Session 4B: System Synthesis

Moderators:

A. Jerraya, TIMA/INPG, France and
B. Lin, Imec, Belgium

This Session is about modeling and synthesis for embedded systems. The first two papers discuss new specification models and their use in simulation and hardware/software codesign. The last paper presents a software scheduling technique for concurrent real-time systems.

A Unified Model for Co-Simulation and Co-Synthesis of Mixed Hardware/Software Systems
  C.A. Valderrama, A. Changuel, P.V. Raghavan, M. Abid, T. Ben Ismail, and A.A. Jerraya

  F. Vahid and D. Gajski

Deadline-Monotonic Software Scheduling for the Co-Synthesis of Parallel Hard Real-Time Systems
  P. Altenbernd