Session 35
Code Generation for Embedded Systems

Chair: Pierre Paulin
Organizers: G. Borriello, M. Sekine

Three critical problems in the hardware/software co-design of embedded systems are the generation of code for embedded processors, the subsequent optimization of the embedded code, and the test and validation of the processor on which the code will execute. Recent work on each of these topics is presented in this session.

35.1 Synthesis of Software Programs for Embedded Control Applications

35.2 Conflict Modelling and Instruction Scheduling in Code Generation for In-House DSP Cores*
Adwin H. Timmer, Marino T.J. Strik, Jef L. van Meerbergen, Jochen A.G. Jess

35.3 Code Optimization Techniques for Embedded DSP Microprocessors
Stan Liao, Srinivas Devadas, Kurt Keutzer, Steve Tjiang, Albert Wang

35.4 Retargetable Self-Test Program Generation Using Constraint Logic Programming
Ulrich Bieker, Peter Marwedel

*Best Paper Award candidate