Session 27
Software Analysis and Synthesis

Chair: Rajesh K. Gupta
Organizers: K. Keutzer, M. Sekine

Hardware-software co-design requires analysis, scheduling, and synthesis of embedded software. These three papers address execution time estimation, scheduling under I/O timing constraints, and synthesis of interface processes.

27.1 Performance Analysis of Embedded Software Using Implicit Path Enumeration
  
  Yau-Tsun Steven Li, Sharad Malik

27.2 Interval Scheduling: Fine-Grained Code Scheduling for Embedded Systems
  
  Pai Chou, Gaetano Borriello

27.3 Interfacing Incompatible Protocols Using Interface Process Generation

  Sanjiv Narayan, Daniel D. Gajski