Session 22
Tutorial: ASIC Prototyping

Chair: Gabriele Saucier - Inst. National Polytech de Grenoble/CSI, Grenoble, France

The tutorial will present the interest and challenges of ASIC prototyping.

Professeur Gabrielle Saucier from Innovative Synthesis Technologies will make a short presentation about the reasons of the strong interest in ASIC prototyping. She will introduce the general scenario of ASIC prototyping and will insist on the software environment required to successfully implement complex ASIC prototyping.

Mr Dasha Estes from Quickturn will introduce briefly the concept of prototyping engines discussing the respective interest of commercial prototyping engines versus proprietary dedicated prototyping boards.

Mr Vincent Olive is from CNET the Research center of France-Telecom. The aim of this center is to study equipment and software tools to support new services and networks. CNET will discuss different styles of rapid prototyping in the development of innovative systems for telecommunications.

Haz Nabulsi is from Sierra Semiconductor which is a manufacturer of high performance, mixed signal integrated circuits systems for multimedia computing and personal communications systems. Sierra will discuss the application of rapid prototyping to several generations of products using Quickturn's emulation systems.

Jack Donovan from Tandem Computers will speak on emulating ASICs within Tandem fault tolerant computers.