Session 5C  Delay and Analog Testing

Chair: Daniel G. Saab

5C.1 On Testing Delay Faults in Macro-Based Combinational Circuits
    Irith Pomeranz, Sudhakar M. Reddy

5C.2 RAFT: A Novel Program for Rapid-Fire Test and Diagnosis of Digital Logic for Marginal Delays and Delay Faults
    Abhijit Chatterjee, Jacob A. Abraham

5C.3 A Comprehensive Fault Macromodel For Opamps
    Chen-Yang Pan, Kwang-Ting Cheng, Sandeep Gupta