The 1995 INTERNATIONAL CONFERENCE ON COMPUTER-AIDED DESIGN will be held November 5-9, 1995.

ICCAD is oriented towards Electrical Engineering CAD professionals, concentrating on CAD for Electronic Circuit Design.

AREAS OF INTEREST

Original technical papers on (but not limited to) the following topics are invited:

1) COMBINATIONAL LOGIC SYNTHESIS: Two-level and Multi-level logic optimization (area, timing, power). FPGA optimization.
   - BDD techniques. Don't care methods. Technology mapping.

2) SEQUENTIAL LOGIC SYNTHESIS AND VERIFICATION: Sequential synthesis and optimization (e.g., state encoding, retiming) for area, timing, and power. Asynchronous design. Formal verification.


5) TIMING AND POWER MODELING AND ANALYSIS: Modeling and analysis techniques for timing and power at the gate and higher levels of abstraction. False path analysis. Transparent latch timing analysis and clock schedule optimization.


8) DISCRETE SIMULATION: Switch, logic and high-level modeling and simulation.

9) ROUTING AND LAYOUT VERIFICATION: Routing for ICs and packages. Performance-driven routing. DRC, ERC, circuit extraction and verification. Symbolic design and compaction.

10) PLACEMENT AND FLOORPLANNING: Partitioning, placement and floorplanning techniques. Area estimation. Module generation and leaf cell synthesis. Complete layout systems (e.g., full chip, MCM). Performance-driven techniques.


Author Information and Format

Authors should submit:
- 1 cover page including:
  - Title of paper.
  - The category 1 - 13 which most closely matches the papers content.
  - Complete name, return address, telephone number, fax number, email address and affiliation of each author.
  - Clear identification of the corresponding author.
  - Papers will be reviewed anonymously. ONLY the cover page should identify the authors and their affiliations.

- 10 copies of one page abstract
  - Abstract, typed on separate page should state clearly and precisely what is new and point out the significant results. The IMPACT, or potential impact, of the contribution will play a major role in evaluation.

- 10 copies of the completed paper not to exceed 20 pages, double-spaced, figures, tables and references included.
  - Papers exceeding 20 pages or previously published will be returned to the authors.

THIS INCLUDES WORKSHOP PROCEEDINGS.

For further information send a one-line email message to: icpubpap@dac.com

AUTHOR'S SCHEDULE

Deadline for submissions:     Postmarked April 10, 1995
Notification of acceptance:   July 5, 1995
Deadline for final version:  August 7, 1995

Proposals for Panel Sessions and Tutorials are invited. Please send complete proposals including the participants to the address listed below.

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