Session V-03: Design Techniques

Chair: Franz J. Rammig, University - GH Paderborn, Germany

In this session novel approaches of VHDL-based design techniques are discussed. VHDL offers substantial support to structure the design process but on the other hand imposes some restrictions. The papers presented are good examples how to handle these tradeoffs.

Implementation of a SDH STM-N IC for B-ISDN Using VHDL Based Synthesis Tools
  Juan Carlos Calderón, Enric Corominas, José M. Tapia, and Lluis Paris

VHDL and Cyclic Corrector Codes
  France Mendez

Generating Compilers for Generated Datapaths
  Michael Held and Manfred Glesner