Session D-09: System and Instruction Set Design

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This session deals with full system design methodologies and automatic instruction set extraction for embedded system design. The first paper describes a success story in system design. The two others present new methods for instruction set design automation.

100-hour Design Cycle: A Test Case
Loganath Ramachandran, Daniel D. Gajski, Sanjiv Narayan, Frank Vahid, and Peter Fung

A Tool for Processor Instruction Set Design
Bruce K. Holmer

Instruction Set Extraction from Programmable Structures
Peter Marwedel and Rainer Leupers