Session D-05: Codesign Architectures

Chair: Jørgen Staunstrup, Technical University of Denmark, Lyngby, Denmark

This session presents different architectures for experimenting with a combined hardware/software realization of a computation.

A Prototyping Environment for Control-Oriented HW/SW Systems using State-Charts, Activity-Charts and FPGA’s
  Klaus Buchenrieder and Christian Veith

A Performance Evaluator for Parameterized ASIC Architectures
  Jie Gong, Daniel D. Gajski, and Alex Nicolau

Hardware-Software-Codesign of Application Specific Microcontrollers with the ASM Environment
  A. Both, B. Biermann, R. Lerch, Y. Manoli, and K. Sievert

A Hardware Environment for Prototyping and Partitioning Based on Multiple FPGAs
  Marc Wendling and Wolfgang Rosensiel