**1994 ACM/IEEE Best Paper Award**

This year, awards are made for the best papers in four categories. Winners are determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque and a cash award of $400. The awards are given by the ACM/SIGDA (Association for Computing Machinery/Special Interest Group on Design Automation, IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDAC (Electronic Design Automation Companies).

**PHYSICAL DESIGN**

Paper 40.2: "Multi-Way Partitioning Via Spacefilling Curves and Dynamic Programming"
Authors: Charles J. Alpert, Andrew B. Kahng
Affiliation: University of California, Los Angeles, CA

**HIGH-LEVEL AND LOGIC SYNTHESIS**

Paper 20.3: "Layout Driven Logic Synthesis For FPGAs"
Authors: Shih-Chieh Chang, Kwang-Ting Cheng, M. Marek-Sadowska
Affiliation: University of California, Santa Barbara, CA

Author: Nam-Sung Woo
Affiliation: AT&T Bell Labs., Murray Hill, NJ

**VERIFICATION, SIMULATION AND TEST**

Paper 7.3: "Resynthesis and Retiming for Optimum Partial Scan"
Authors: Srimat T. Chakradhar, Sujit Dey
Affiliation: NEC USA C&C Research Labs., Princeton, NJ

**USE OF CAD AND CAD SYSTEMS**

Paper 17.3: "Basic Concept of Cooperative Timing-Driven Design Automation Technology for High-Speed RISC Processor: HARP-1"
Authors: Hidekazu Terai, Kazutoshi Gemma, Yasuhiro Ohno
Affiliation: Hitachi, Ltd., Kanagawa-ken, Japan

Authors: Yohsuke Nagao, Yasuo Satoh
Affiliation: Hitachi, Ltd., Tokyo, Japan

**1994 IEEE FELLOWS**

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