Session 44: Discrete Simulation
Chair: Karem Sakallah

The first paper in this session describes a new switch-level CMOS model that provides better signal resolution than previous models and is, thus, more suitable for stuck-on fault coverage. The other two papers describe new ways of estimating signal activity in digital circuits, with a focus on improving the estimation accuracy without an inordinate increase in computer time. Accurate calculation of signal activity is essential for power estimation.

44.1 MODELING OF INTERMEDIATE NODE STATES IN SWITCH-LEVEL NETWORKS
    Peter Dahlgren, Peter Liden

44.2 STATISTICAL ESTIMATION OF THE SWITCHING ACTIVITY IN DIGITAL CIRCUITS
    Michael G. Xakellis, Farid N. Najm

44.3 IMPROVING THE ACCURACY OF CIRCUIT ACTIVITY MEASUREMENT
    Bhanu Kapoor