Session 42: Sequential Synthesis

Chair: Fabio Somenzi

The first two papers in this session address classic problems in FSM synthesis. The third paper addresses a very practical problem that must be addressed when sequential circuits are actually implemented.

42.1 PERMISSIBLE OBSERVABILITY RELATIONS IN FSM NETWORKS
Huey-Yih Wang, Robert K. Brayton

42.2 A FULLY IMPLICIT ALGORITHM FOR EXACT STATE MINIMIZATION
Timothy Kam, Tiziano Villa, Robert Brayton, A. Sangiovanni-Vincentelli

42.3 BOOLEAN MATCHING OF SEQUENTIAL ELEMENTS
Shankar Krishnamoorthy, Frederic Mailhot