Session 38: Formal Verification of Systems
Chair: Carl-Johan Seger

This session presents advances in the area of verifying systems. The first two papers deal with the verification of microprocessors. The next two deal with reducing the state space of large systems.

38.1 FORMALLY VERIFYING A MICROPROCESSOR USING A SIMULATION METHODOLOGY
Derek L. Beatty, Randal E. Bryant

38.2 AUTOMATIC VERIFICATION OF PIPELINED MICROPROCESSORS
Vishal Bhagwati, Srinivas Devadas

38.3 A TIME ABSTRACTION METHOD FOR EFFICIENT VERIFICATION OF COMMUNICATING SYSTEMS
Eric Verlind, Tilman Kolks, Gjalt de Jong, Bill Lin, Hugo De Man

38.4 ON THE COMPUTATION OF THE SET OF REACHABLE STATES OF HYBRID MODELS
A.S. Krishnakumar, Kwang-Ting Cheng