Session 18: BDD Techniques and Formal Verification

Chair: Ellen M. Sentovich

This session examines advances in using BDDs for formal verification and related applications. The first paper examines a new application for implicit state space exploration, the others examine techniques to speed up BDD-based state space search algorithms.

18.1 PROBABILISTIC ANALYSIS OF LARGE FINITE STATE MACHINES
Gary D. Hachtel, Enrico Macii, Abelardo Pardo, Fabio Somenzi

18.2 NEW TECHNIQUES FOR EFFICIENT VERIFICATION WITH IMPLICITLY CONJOINED BDDS
Alan J. Hu, Gary York, David L. Dill

18.3 BDD VARIABLE ORDERING FOR INTERACTING FINITE STATE MACHINES
Adnan Aziz, Serdar Tasiran, Robert K Brayton

18.4 AUXILIARY VARIABLES FOR EXTENDING SYMBOLIC TRAVERSAL TECHNIQUES TO DATA PATHS
Gianpiero Cabodi, Paolo Camurati, Stefano Quer