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Emerging Challenges in Assuring Functional Correctness of VLSI Devices for Mobile Applications

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Abstract

Embedded HW and SW complexity, area, power and time to market for mobile device era applications are rising at a rate faster than that of devices in the PC era applications. Further, conforming to functional correctness to attain high levels of functional reliability is becoming increasingly difficult. Thus, new methods in design, specification and verification of functionality are required to satisfy these requirements. In this talk, we will explore the motivation to solve these problems, and review some of the current practice and research in industry to address them.

Biography

Scott Runner is currently the Director of the Design Verification team in Qualcomm CDMA Technologies, the semiconductor division of Qualcomm. In his twenty one years in the industry, he has held positions as Director of Design IP, Design Automation and Design Manager and Engineer at Conexant Systems, Synopsys, and Fujitsu Microelectronics. He has taped out forty one devices, authored twenty three papers and articles and held several conference and committee chair and membership positions. He holds BS in Physics and Computer Science from Georgia Tech, and post graduate work in Engineering Management, Operating Systems and EE from Georgia Tech and Portland State University.

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