

# COLLOQUIUM

## Center for Embedded Computer Systems

*Presents*

### Embedded Control Software Engineering Methods and Tools Research at ISL, Bangalore

**Dr. S. Ramesh**

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#### *Abstract*

This talk will highlight the R&D work that is being carried out at the General Motors India Science Lab. The work is in the broad area of Control Software Engineering Methods and Tools for future automotive embedded systems. Some focus areas are formal method based requirement engineering, formal framework for time triggered distributed implementations and Translator validation.

#### *Biography*

S. Ramesh has more than 15 years of research experience in the areas of high level language design, validation and verification of distributed systems, embedded software and hardware designs. Currently he is a technical fellow at the General Motors India Science Laboratory in Bangalore. As a technical fellow, he plays a key role in setting up a Centre of Excellence in rigorous control software engineering for automotive embedded systems. Earlier, he was a full Professor in the Dept of Computer Science and Engineering, Indian Institute of Technology, Bombay. At IIT Bombay, he was also the head of the Centre for Formal Design and Verification of Software which he co-founded. As part of the centre activities, he carried out many industry-sponsored projects on verification of hardware and embedded software.

**Friday, March 16, 2007**

Calit2 Auditorium

Talk begins at 3:00pm; Refreshments at 2:30pm

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