



Center for Embedded and Cyber-Physical Systems
University of California, Irvine

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Technical Report CECS-16-04
September 7, 2016

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Abstract

Bitcoin is a new digital asset, and for years people are struggling to accelerate Bitcoin miners for more profit. In this document, we present an example of a parallel Bitcoin miner specification model as a case study for SystemC based Electronic System Level design. We recode a C++ reference code of a Bitcoin miner, implement a central controller and utilize polling to synchronize between the parallel computation blocks. The experiment demonstrates that SystemC is a powerful language for system design, and the results show the speed-up ratio grows linearly with the number of parallel worker modules, which in turn suggests our design to be effective on accelerating the Bitcoin miner.

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Bitcoin is a new digital asset, and for years people are struggling to accelerate Bitcoin miners for more profit. In this document, we present an example of a parallel Bitcoin miner specification model as a case study for SystemC based Electronic System Level design. We recode a C++ reference code of a Bitcoin miner, implement a central controller and utilize polling to synchronize between the parallel computation blocks. The experiment demonstrates that SystemC is a powerful language for system design, and the results show the speed-up ratio grows linearly with the number of parallel worker modules, which in turn suggests our design to be effective on accelerating the Bitcoin miner.

1 Introduction

*Bitcoin is a new peer-to-peer digital asset and a decentralized payment system introduced by Satoshi Nakamoto in 2009 [5]. New bitcoins are created from Bitcoin blocks, which must contain some transactions and a so-called *proof-of-work*. The proof-of-work requires users to find a *nonce*, such that when the block content is hashed using the SHA256 [7] along with the number, the result is numerically smaller than the network's *difficulty target*.*

The proof-of-work is easy for any node in the network to verify, but extremely time-consuming to generate, as for a secure cryptographic hash, users must try many different nonce values (usually the sequence of tested values is 0, 1, 2, 3, ...) before meeting the difficulty target [2]. As such, with a higher hash-rate,

users are expected to earn more bitcoins.

In this paper, we propose a SystemC [6] based parallel Bitcoin miner model. Our Bitcoin miner is based on a C++ reference code [4], and using polling to synchronize between the parallel computation blocks. Timing for communications and computations are studied under different conditions using the single-thread SystemC 2.3.1-Accellera simulator.

2 Overview of Bitcoin Miner Algorithm

Mining today takes on two forms, solo mining and pooled mining [8]. The proposed Bitcoin miner mainly focuses on solo mining and is based on the reference implementation CPUminer [4].

The Bitcoin miner basically performs the following three steps, as illustrated in Figure 1:

I. **get_work** first requests a *Bitcoin block template* from the network or a local server, and based on that, it builds a block header which contains some information describing the Bitcoin block, as described in [1]. Then the 80-byte block header is sent to the computational part, called `scan_and_hash` in our project, along with a target threshold.

II. **scan_and_hash** iterates the nonce (a 4-byte value inside the block header) over a certain range, and repeatedly performs SHA256 on the whole block header to generate a corresponding hash. When the hash value is below the desired difficulty target, the proof-of-work is done and the successful block header is transferred to the `submit_work` module.

III. **submit_work** combines together the block header

Value	Description
0x123...fff	First 72 bytes which are fixed
0x30c31b18	nBit, encoded version of target
0x00000000	Initial nonce value

Table 1: Example of a Bitcoin block header

and some other information. Then it broadcasts the block to the network, or sends it back to the local server.

Note that the main computation of Bitcoin mining is performed in `scan_and_hash`. In other words, `scan_and_hash` carries by far the highest computational work.

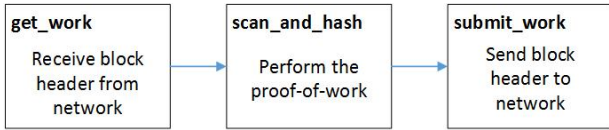


Figure 1: Bitcoin miner flowchart

3 System Level Modeling of Bitcoin Miner

In this section, we first introduce the implementation of a sequential Bitcoin miner, which serves both as a simple model of mining and as a reference for performance analysis. Then we propose our high level model of parallel Bitcoin miner in the SystemC system-level description language, and estimate its expected speed-up ratio through simulation.

3.1 Sequential Bitcoin Miner Design

Figure 2 demonstrates the mining steps of a sequential `scan_and_hash` block. As illustrated, there is a big while loop iterating the nonce and performing SHA256. For instance, assume that the block header passed to the `scan_and_hash` block is shown in Table 3. Then the goal of the `scan_and_hash` would be to find a nonce starting from the initial value (0x00000000 here) such that the hash of the 80-byte block header is less than the difficulty target. This target is derived from the nBit, and

more details are explained in [3]. In this example, the target is $0x1bc330 * 256^{0x18-3}$. We should note that the sequential miner is very time consuming.

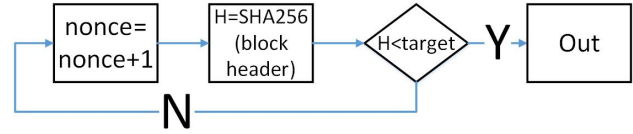


Figure 2: `scan_and_hash` for sequential implementation

3.2 Parallel Bitcoin Miner Design

Based on the observation that the `scan_and_hash` function is the most complex and time-consuming part, we decide to optimize it first. Parallelization is the most intuitive approach to speed up the computation.

The parallel structure we propose is that to duplicate the sequential one and run them in parallel, with each assigned a disjoint nonce range, and synchronized through a central controller named `main_control_block`, as shown in Figure 4

3.2.1 Expected speed-up ratio of parallelization

To ease qualitative analysis of the speed-up ratio, it is reasonable to assume that there is a one-to-one mapping between the nonce and the resulting hash, and thus we can easily calculate the probability of finding a successful nonce. In the above example in Table 3, this probability is roughly $2^{(0x18-3)*8+24-256} = 5.4^{-20}$. This is to say that if we can calculate 1 billion nonces per second, it will take around 588 years on average to find a successful block header. Furthermore, note that with SHA256 as the secure cryptographic hash function, the successful nonces have a uniform distribution across the entire range.

Based on these two points, the speed-up ratio for our parallel design is expected to linearly grow with the increasing of parallelism, as illustrated through simulation in Figure 3. For instance, with the parallelism

of two, such that one scan_and_hash block iterates from 0x0000_0000 to 0x7fff_ffff and the other one from 0x8000_0000 to 0xffff_ffff, this two-worker Bitcoin miner is expected to spend half as much time as that of the sequential one to find a successful nonce.

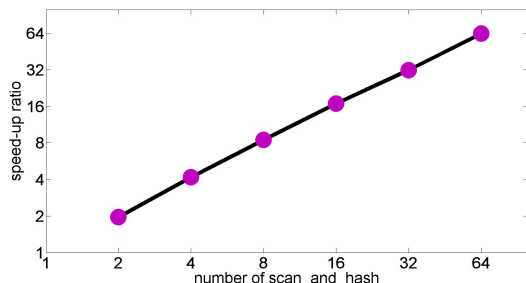


Figure 3: Expected speed-up ratio with increasing level of parallelism

3.2.2 Implementation of Parallel Bitcoin Miner

In this section, we propose the implementation of our parallel Bitcoin miner. The flowchart is shown in Figure 4, which is mainly different from the sequential implementation in the place of an additional main_control_block.

Figure 5 shows the main_control_block, and for a simple illustrative purpose, this figure only contains two scan_and_hash blocks. The main_control_block serves to synchronize the parallel scan_and_hash blocks, such that when one of them has found a successful block header, others can stop as they do not need to work on the current proof-of-work any longer. And in the next few clock cycles, all the scan_and_hash blocks shall restart with a new block header. For now, the synchronization is implemented through polling.

Here we describe in more details the algorithm for the main_control_block (controller). First, the controller requests a 80-byte block header from the outside. Next, **FOUND** is sent to all the scan_and_hash blocks (workers), which indicates that the workers shall start (restart, if the worker is already running) on a new proof-of-work, and then block header is broadcasted. Afterwards, synchronization is performed within a polling loop. The controller begins to wait for flags from workers. If one or more **FOUND** is received,

which means a successful nonce is found, then the controller sends the nonce out, and restarts itself. If the flags are all **NOT FOUND**, then **NOT FOUND** flag is broadcasted back, indicating that all workers shall keep hashing on the current proof-of-work. Correspondingly, we also modify the algorithm for the scan_and_hash block. As shown in Figure 6, this block consists of a single loop. At the very beginning of the loop, a flag is received from the controller. If **FOUND**, then it reads in a block header, and increments the nonce by one and performs SHA256 for the proof-of-work. If **NOT FOUND**, the module simply skips the reading of the block header, and leaves the computation steps unchanged.

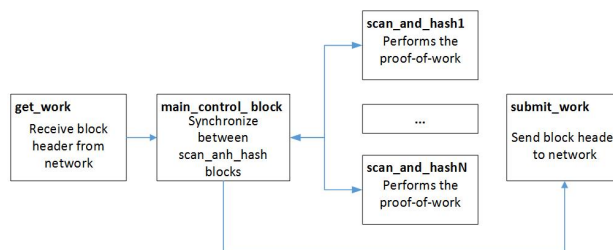


Figure 4: Structure of parallel Bitcoin miner

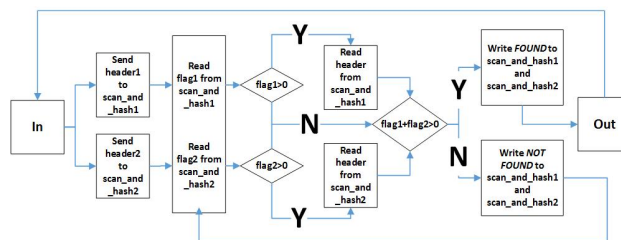


Figure 5: Structure of main_control_block with two scan_and_hash blocks

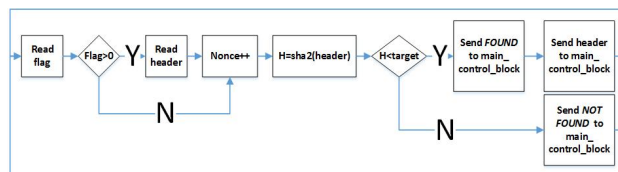


Figure 6: Structure of scan_and_hash for parallel implementation

4 Experiments and Results

We have implemented the proposed Bitcoin miner in SystemC 2.3.1 and simulated the module under an Intel E3-1240 processor, with a main frequency of 3.40 GHz. The simulator we choose is the SystemC 2.3.1-Accellera.

Our main concern for now is the speed-up ratio of the proposed Bitcoin miner, and the analysis is performed in two parts.

In the first part, we focus on the timing of synchronization between the scan_and_hash blocks, and it is easily obtained by appending wait(1,SC_NS) instructions to the communication behaviors, specifically, the *write* instructions. The final simulator time is recorded as N_{comm} , which indicates the number of communications.

In the second part, the timing cost for computation is studied. This is done in a similar way by appending wait(1,SC_NS) instructions to the computation behaviors. Note that in this step, the *wait* instructions for communications are removed, so the simulation time only increases with the computation behaviors. Similarly, the final simulator time is recorded as N_{comp} , which indicates the number of computations.

Although we have set the cost of communication and computation both to one nano second, they are quite different in reality, and the reason we set them to the same value is merely to check out the relationship between the total number of communications and computations taking place in the simulation. And furthermore, for the complexity of communications, the time overhead of polling is not taken into account in the calculation of the speed-up ratio.

Table 2 evaluates the performance of the proposed Bitcoin miner, where P is the number of scan_and_hash blocks. The reference for the speed-up ratio is the sequential model, and all the experiments are conducted with a total of 500 proof-of-works. The results reflect the perfect parallelization of our design, which is in accord with the expected ones mentioned in section 3.2.1. Note that our current simulator only runs on a single thread, hence the run times are roughly the same under different number of workers. And since the proof-of-work is a matter of guessing a random number, the differences between the run times are rea-

P	N_{comp}	Speed-up ratio	run time (s)
sequential	33,799,928	1	26.411
2	16,507,948	2.047	24.448
4	8,718,065	3.877	25.283
8	4,186,589	8.073	23.695

Table 2: Performance with increasing Number of Workers

P	N_{comp}	N_{comm}	$N_{\text{comm}}/N_{\text{comp}}$
sequential	33,799,928	66,549,037	1.969
2	16,507,948	67,354,978	4.080
4	8,718,065	64,063,356	7.348
8	4,186,589	65,448,400	15.633

Table 3: Relationships between the Total Number of Communications and Computations

sonable.

Table 3 shows the relationship between the total number of communications and computations. From the structure of our design shown in Figure 5, we can deduce that N_{comm} is expected to be $2 \times P \times N_{\text{comp}}$. This table illustrates that the experimental result agrees well with the theoretical one, which also in turn indicates that our design is very good in parallelism.

5 Conclusion and Future Work

In this document, we have created a system-level model of a bitcoin miner based on its C++ reference code. We improved the model by exploiting parallelization. The SystemC 2.3.1-Accellera simulator is used to find the speed-up ratio of our design, which grows almost linearly with the number of scan_and_hash blocks. In future work, we plan to improve the hash-rate by combining thread-level parallelism with data-level parallelism, as well as introducing some better synchronization methods. Researches about power and area may also be taken into consideration.

References

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<https://en.wikipedia.org/wiki/Bitcoin>.
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A Appendix

A.1 Source Code of Parallel Bitcoin Miner in SystemC

Listing 1: config.h

```
1 #ifndef _CONFIG_H
2 #define _CONFIG_H
3
4 #define PAR 8
5 #define N PAR
6 #define BUF_SIZE1 2048
7 #define SET_STACK_SIZE()      set_stack_size(128*1024*1024)
8
9
10 #define OPT_N_THREADS 1;
11 #define OPT_SCANTIME 5;
12 #define OPT_QUIET true;
13 #define OPT_BENCHMARK true
14
15
16 #endif
```

Listing 2: types.h

```
1 #ifndef _TYPES
2 #define _TYPES
3
4
5 #include <inttypes.h>
6 #include <stdio.h>
7 #include <stdlib.h>
8 #include <math.h>
9 #include <string.h>
10 #include <sys/time.h>
11 #include <time.h>
12
13 #include "systemc.h"
14 #include "config.h"
15
16 #define bswap_32(x) (((x) << 24) & 0xff000000u | ((x) << 8) & 0x00ff0000u) \
17 | (((x) >> 8) & 0x0000ff00u) | (((x) >> 24) & 0x000000ffu)
18
19 typedef struct work {
20     uint32_t data[32];
21     uint32_t target[8];
22
23     /*
24      int height;
25      char* txs;
26      char* workid;
27
28      char* job_id;
29      size_t xnonce2_len;
30      unsigned char* xnonce2;
31     */
32
33     work(void)
34     {
35         for (int i = 0; i < 32; i++)
36             data[i] = 0;
37         for (int i = 0; i < 8; i++)
38             target[i] = 0;
39
40     }
41 }
42
```

```

43     work& operator=(const work& copy)
44     {
45         for (int i = 0; i < 32; i++)
46             data[i] = copy.data[i];
47
48         for (int i = 0; i < 8; i++)
49             target[i] = copy.target[i];
50
51         return *this;
52     }
53
54     uint32_t& operator[](const int index)
55     {
56         return data[index];
57     }
58
59     operator uint32_t*()
60     {
61         return data;
62     }
63
64 } WORK;
65
66
67
68 typedef struct scan_work {
69     uint32_t data[32];
70     uint32_t target[8];
71
72     uint32_t max_nonce;
73     unsigned long hashes_done;
74
75     scan_work(void)
76     {
77         for (int i = 0; i < 32; i++)
78             data[i] = 0;
79         for (int i = 0; i < 8; i++)
80             target[i] = 0;
81         hashes_done=0;
82     }
83
84     scan_work& operator=(const scan_work& copy)
85     {
86         for (int i = 0; i < 32; i++)
87             data[i] = copy.data[i];
88
89         for (int i = 0; i < 8; i++)
90             target[i] = copy.target[i];
91         hashes_done=copy.hashes_done;
92         return *this;
93     }
94
95     uint32_t& operator[](const int index)
96     {
97         return data[index];
98     }
99
100    operator uint32_t*()
101    {
102        return data;
103    }
104
105 } SCAN_WORK;
106
107 typedef struct config_scan_work {
108     uint32_t start_point;
109     int index;
110
111
112     config_scan_work(void)

```

```

113     {
114         start_point=0;
115         index=0;
116     }
117
118     config_scan_work& operator=(const config_scan_work& copy)
119     {
120         start_point=copy.start_point;
121         index=copy.index;
122         return *this;
123     }
124
125     uint32_t& operator[](const int index)
126     {
127         return start_point;
128     }
129
130     operator uint32_t*()
131     {
132         return &start_point;
133     }
134
135 } CONFIG_SCAN_WORK;
136
137
138
139
140 template<class T> class sc_T_sender: virtual public sc_interface
141 {
142 public:
143     virtual void write(T) = 0;
144 };
145
146 template<class T> class sc_T_receiver: virtual public sc_interface
147 {
148 public:
149     virtual void read(T&) = 0;
150 };
151
152 template<class T> class sc_T_queue: public sc_channel,
153     public sc_T_sender<T>, public sc_T_receiver<T>
154 {
155 public:
156     sc_T_queue(sc_module_name name, int size_ = 4): sc_channel(name)
157     {
158         size = size_;
159         buf = (T*)malloc(sizeof(T)*size);
160         reset();
161     }
162
163     ~sc_T_queue()
164     {
165         delete [] buf;
166     }
167
168     int num_available()
169     {
170         return size - free_slots;
171     }
172
173     int num_free()
174     {
175         return free_slots;
176     }
177
178     void write(T X)
179     {
180         if (num_free() == 0)
181             wait(data_read_event);
182         buf[wi] = X;

```

```

183         wi = (wi+1) % size;
184         free_slots --;
185         data_written_event.notify(SC_ZERO_TIME);
186     }
187
188     void read(T& Y)
189     {
190         if (num_available() == 0)
191             wait(data_written_event);
192         Y = buf[ri];
193         ri = (ri+1) % size;
194         free_slots ++;
195         data_read_event.notify(SC_ZERO_TIME);
196     }
197
198     void reset()
199     {
200         free_slots = size;
201         ri = 0;
202         wi = 0;
203     }
204
205 private:
206     int size;           // size
207     T *buf;            // circular buffer
208     int free_slots;    // free space
209     int ri;            // read index
210     int wi;            // write index
211
212     sc_event data_read_event;
213     sc_event data_written_event;
214 };
215
216
217 typedef sc_T_sender< WORK> i_work_sender;
218 typedef sc_T_receiver< WORK> i_work_receiver;
219 typedef sc_T_queue< WORK> c_work_queue;
220
221 typedef sc_T_sender<unsigned> i_uint_sender;
222 typedef sc_T_receiver<unsigned> i_uint_receiver;
223 typedef sc_T_queue<unsigned> c_uint_queue;
224
225 typedef sc_T_sender< SCAN_WORK> i_scan_work_sender;
226 typedef sc_T_receiver< SCAN_WORK> i_scan_work_receiver;
227 typedef sc_T_queue< SCAN_WORK> c_scan_work_queue;
228
229 typedef sc_T_sender<int> i_num_sender;
230 typedef sc_T_receiver<int> i_num_receiver;
231 typedef sc_T_queue<int> c_num_queue;
232
233
234 typedef sc_T_sender< CONFIG_SCAN_WORK> i_config_scan_work_sender;
235 typedef sc_T_receiver< CONFIG_SCAN_WORK> i_config_scan_work_receiver;
236 typedef sc_T_queue< CONFIG_SCAN_WORK> c_config_scan_work_queue;
237
238
239 static inline uint32_t swab32(uint32_t v) //0x12345678 => 0x78563412
240 {
241
242     return bswap_32(v);
243 }
244
245
246
247 #endif

```

Listing 3: getwork.h

```
1 #ifndef _GET_WORK
```

```

2 #define _GET_WORK
3
4 #include "systemc.h"
5 #include "types.h"
6
7
8
9 class Get_Work : public sc_module
10 {
11
12 public:
13     sc_port<i_work_sender> WorkOut;
14
15
16     struct work g_work; //this should be shared between many scanhash blocks
17
18     //CONSTRUCTOR
19     SC_HAS_PROCESS(Get_Work);
20     Get_Work(sc_module_name name);
21
22     //METHODS
23     bool workio_get_work(uint32_t p,uint32_t t);
24
25     void main();
26 };
27
28
29 #endif

```

Listing 4: getwork.cc

```

1 #include "getwork.h"
2
3 Get_Work::Get_Work(sc_module_name name)
4 : sc_module(name)
5 {
6     SC_THREAD(main);
7     SET_STACK_SIZE();
8 }
9
10 bool Get_Work::workio_get_work(uint32_t p,uint32_t t)
11 {
12     //now this is for benchmark only
13
14
15     int failures = 0;
16
17     memset(g_work.data, 0x55, 76);
18     g_work.data[17] = swab32(time(NULL))+t*10; //include here
19
20     memset(g_work.data + 19, 0x00, 52); //19-31 uint32_t
21     g_work.data[20] = 0x80000000;
22     g_work.data[31] = 0x00000280;
23     memset(g_work.target, 0x00, sizeof(g_work.target)); //256 bit
24     for(int j=0;j<7;j++)
25         g_work.target[j]=0xffffffff;
26     g_work.target[7]=p;
27
28     return true;
29 }
30
31
32
33
34 void Get_Work::main()
35 {
36
37     bool ok = true;
38     int i=0;

```

```

39     uint32_t n=0x0000ffff;
40     while(i<500){
41         ok = workio_get_work(n,i);
42
43         i=i+1;
44
45         WorkOut->write(g_work);
46     }
47
48 }

```

Listing 5: miner.h

```

1  #ifndef _Miner
2  #define _Miner
3
4  #include "systemc.h"
5  #include "types.h"
6
7  #include "miner_thread.h"
8  #include "scan_hash.h"
9
10
11 #define QUEUES_AND_SCANNER_CTOR(n) \
12     c_num_queue Flag_to_scanner_q##n; \
13     c_num_queue Flag_from_scanner_q##n; \
14     c_scan_work_queue Work_to_scanner_q##n; \
15     c_scan_work_queue Work_from_scanner_q##n; \
16     c_config_scan_work_queue Config_q##n; \
17     Scan_Hash scan_hash##n; \
18
19
20
21 #define QUEUES_AND_SCANNER_INIT(n) \
22     , Flag_to_scanner_q##n ("Flag_to_scanner_q" #n, BUF_SIZE1) \
23     , Flag_from_scanner_q##n ("Flag_from_scanner_q" #n, BUF_SIZE1) \
24     , Work_to_scanner_q##n ("Work_to_scanner_q" #n, BUF_SIZE1) \
25     , Work_from_scanner_q##n ("Work_from_scanner_q" #n, BUF_SIZE1) \
26     , Config_q##n ("Config_q" #n, BUF_SIZE1) \
27     , scan_hash##n ("scan_hash" #n) \
28
29
30 #define QUEUES_AND_SCANNER_CONNECTION(n) \
31     miner_thread.flag_to_scanner_##n (Flag_to_scanner_q##n); \
32     miner_thread.flag_from_scanner_##n (Flag_from_scanner_q##n); \
33     miner_thread.work_to_scanner_##n (Work_to_scanner_q##n); \
34     miner_thread.work_from_scanner_##n (Work_from_scanner_q##n); \
35     miner_thread.config_to_scanner_##n (Config_q##n); \
36     scan_hash##n.flag_to_main (Flag_from_scanner_q##n); \
37     scan_hash##n.work_to_main (Work_from_scanner_q##n); \
38     scan_hash##n.flag_from_main (Flag_to_scanner_q##n); \
39     scan_hash##n.work_from_main (Work_to_scanner_q##n); \
40     scan_hash##n.config_from_main (Config_q##n); \
41
42
43 class Miner : public sc_module
44 {
45
46 public:
47
48
49     QUEUES_AND_SCANNER_CTOR(0)
50 #if PAR > 1
51     QUEUES_AND_SCANNER_CTOR(1)
52 #endif
53 #if PAR > 2
54     QUEUES_AND_SCANNER_CTOR(2)
55     QUEUES_AND_SCANNER_CTOR(3)
56 #endif

```



```

57 #if PAR > 4
58     QUEUES_AND_SCANNER_CTOR(4)
59     QUEUES_AND_SCANNER_CTOR(5)
60     QUEUES_AND_SCANNER_CTOR(6)
61     QUEUES_AND_SCANNER_CTOR(7)
62 #endif
63
64     sc_port<i_work_receiver> WorkIn;
65     sc_port<i_work_sender>   WorkOut;
66
67
68     /*
69     miner_thread send work to scan_hash
70     scan_hash hashes and send back hash to miner_thread
71     */
72     Miner_Thread  miner_thread;
73
74
75     SC_HAS_PROCESS(Miner);
76
77     //CONSTRUCTOR
78     Miner(sc_module_name name);
79
80     //METHODS
81 };
82
83 #endif

```

Listing 6: miner.cc

```

1 #include "miner.h"
2
3 Miner::Miner(sc_module_name name)
4 : sc_module(name)
5 , miner_thread("miner_thread")
6     QUEUES_AND_SCANNER_INIT(0)
7 #if PAR > 1
8     QUEUES_AND_SCANNER_INIT(1)
9 #endif
10 #if PAR > 2
11     QUEUES_AND_SCANNER_INIT(2)
12     QUEUES_AND_SCANNER_INIT(3)
13 #endif
14 #if PAR > 4
15     QUEUES_AND_SCANNER_INIT(4)
16     QUEUES_AND_SCANNER_INIT(5)
17     QUEUES_AND_SCANNER_INIT(6)
18     QUEUES_AND_SCANNER_INIT(7)
19 #endif
20
21 {
22     miner_thread.WorkIn(WorkIn); //get work from getwork
23
24     miner_thread.WorkOut(WorkOut); //send result to submitwork
25
26     QUEUES_AND_SCANNER_CONNECTION(0)
27 #if PAR > 1
28     QUEUES_AND_SCANNER_CONNECTION(1)
29 #endif
30 #if PAR > 2
31     QUEUES_AND_SCANNER_CONNECTION(2)
32     QUEUES_AND_SCANNER_CONNECTION(3)
33 #endif
34 #if PAR > 4
35     QUEUES_AND_SCANNER_CONNECTION(4)
36     QUEUES_AND_SCANNER_CONNECTION(5)
37     QUEUES_AND_SCANNER_CONNECTION(6)
38     QUEUES_AND_SCANNER_CONNECTION(7)
39 #endif

```

```
40 }
41 }
```

Listing 7: miner_thread.h

```
1  #ifndef _MINER_THREAD
2  #define _MINER_THREAD
3
4  #include "systemc.h"
5  #include "types.h"
6
7  #define PORT_AND_CONFIG_CTOR(n) \
8      sc_port<i_num_sender> flag_to_scanner_##n; \
9      sc_port<i_num_receiver> flag_from_scanner_##n; \
10     sc_port<i_scan_work_sender> work_to_scanner_##n; \
11     sc_port<i_scan_work_receiver> work_from_scanner_##n; \
12     sc_port<i_config_scan_work_sender> config_to_scanner_##n; \
13     struct config_scan_work config_##n; \
14
15
16
17 #define SEND_START_MSG(n) \
18     int flag_##n; \
19     flag_to_scanner_##n->write(1); \
20     wait(1,SC_NS); \
21
22 #define SEND_CONFIG_AND_WORK(n) \
23     config_##n.start_point = max_nonce/N*n; \
24     config_##n.index = n; \
25     work_to_scanner_##n->write(scan_work); \
26     wait(1,SC_NS); \
27     config_to_scanner_##n->write(config_##n); \
28     wait(1,SC_NS); \
29
30 #define POLLING(n) \
31     flag_from_scanner_##n->read(flag_##n); \
32     wait(1,SC_NS); \
33     if(flag_##n>0){ \
34         flag_glb=1; \
35         work_from_scanner_##n->read(scan_work); \
36         wait(1,SC_NS); \
37         printf("result_from_scanner_%d\n",n); \
38     } \
39
40
41 class Miner_Thread : public sc_module
42 {
43
44 public:
45
46     PORT_AND_CONFIG_CTOR(0)
47 #if PAR > 1
48     PORT_AND_CONFIG_CTOR(1)
49 #endif
50 #if PAR > 2
51     PORT_AND_CONFIG_CTOR(2)
52     PORT_AND_CONFIG_CTOR(3)
53 #endif
54 #if PAR > 4
55     PORT_AND_CONFIG_CTOR(4)
56     PORT_AND_CONFIG_CTOR(5)
57     PORT_AND_CONFIG_CTOR(6)
58     PORT_AND_CONFIG_CTOR(7)
59 #endif
60     sc_port<i_work_receiver> WorkIn;
61     sc_port<i_work_sender> WorkOut;
62
63
64     struct work g_work; //this should be shared between many scanhash blocks
```

```

65     struct scan_work scan_work;
66     double thr_hashrate;
67
68
69     //CONSTRUCTOR
70     SC_HAS_PROCESS(Miner_Thread);
71     Miner_Thread(sc_module_name name);
72
73     //METHODS
74     void miner_thread();
75
76     void main();
77 };
78
79
80 #endif

```

Listing 8: miner_thread.cc

```

1  #include "miner_thread.h"
2  #include "util.h"
3
4
5
6
7  Miner_Thread::Miner_Thread(sc_module_name name)
8  : sc_module(name)
9  {
10     SC_THREAD(main);
11     SET_STACK_SIZE();
12 }
13
14
15
16 void work_copy(struct work *dest, const struct work *src)
17 {
18     memcpy(dest, src, sizeof(struct work));
19 }
20
21
22
23 void Miner_Thread::miner_thread()
24 {
25
26
27     int thr_id = 0;
28     struct work work;
29     uint32_t max_nonce;
30     uint32_t end_nonce = 0xffffffff - 0x20;
31
32     char s[16]; //used for sprintf
33
34     thr_hashrate=0;
35     time_t g_work_time = time(NULL);
36
37
38     unsigned long hashes_done;
39     struct timeval tv_start, tv_end, diff;
40     int64_t max64;
41
42
43     SEND_START_MSG(0)
44 #if PAR > 1
45     SEND_START_MSG(1)
46 #endif
47 #if PAR > 2
48     SEND_START_MSG(2)
49     SEND_START_MSG(3)
50 #endif

```

```

51 #if PAR > 4
52     SEND_START_MSG(4)
53     SEND_START_MSG(5)
54     SEND_START_MSG(6)
55     SEND_START_MSG(7)
56 #endif
57     int simulation_loop=0;
58
59     while(1){
60         simulation_loop++;
61         WorkIn->read(g_work);
62         int flag_glb=0;
63         printf("target=_\n");
64         for(int m=0;m<8;m++)
65             printf("%08x",g_work.target[m]);
66         printf("\n");
67         /*preprocessing before send work*/
68
69         work_copy(&work, &g_work);
70
71         work.data[19] = 0;
72
73
74         max_nonce = end_nonce;
75
76         hashes_done = 0; //hashes_done is the number of hashes scanned
77         gettimeofday(&tv_start, NULL);
78
79
80         for(int j=0;j<32;j++)
81             scan_work.data[j]=work.data[j];
82         for(int j=0;j<8;j++)
83             scan_work.target[j]=work.target[j];
84         scan_work.max_nonce=max_nonce;
85         scan_work.hashes_done=hashes_done;
86
87         SEND_CONFIG_AND_WORK(0)
88     #if PAR > 1
89         SEND_CONFIG_AND_WORK(1)
90     #endif
91     #if PAR > 2
92         SEND_CONFIG_AND_WORK(2)
93         SEND_CONFIG_AND_WORK(3)
94     #endif
95     #if PAR > 4
96         SEND_CONFIG_AND_WORK(4)
97         SEND_CONFIG_AND_WORK(5)
98         SEND_CONFIG_AND_WORK(6)
99         SEND_CONFIG_AND_WORK(7)
100     #endif
101
102
103     while(true){
104
105         POLLING(0)
106     #if PAR > 1
107         POLLING(1)
108     #endif
109     #if PAR > 2
110         POLLING(2)
111         POLLING(3)
112     #endif
113     #if PAR > 4
114         POLLING(4)
115         POLLING(5)
116         POLLING(6)
117         POLLING(7)
118     #endif
119
120

```

```

121         if(flag_glb>0){
122             flag_to_scanner_0->write(1);wait(1,SC_NS);
123
124             #if PAR > 1
125                 flag_to_scanner_1->write(1);wait(1,SC_NS);
126             #endif
127             #if PAR > 2
128                 flag_to_scanner_2->write(1);wait(1,SC_NS);
129                 flag_to_scanner_3->write(1);
130                 wait(1,SC_NS);
131             #endif
132             #if PAR > 4
133                 flag_to_scanner_4->write(1);wait(1,SC_NS);
134                 flag_to_scanner_5->write(1);wait(1,SC_NS);
135                 flag_to_scanner_6->write(1);wait(1,SC_NS);
136                 flag_to_scanner_7->write(1);wait(1,SC_NS);
137             #endif
138             break;
139         }
140
141         flag_to_scanner_0->write(0);wait(1,SC_NS);
142     #if PAR > 1
143         flag_to_scanner_1->write(0);wait(1,SC_NS);
144     #endif
145     #if PAR > 2
146         flag_to_scanner_2->write(0);wait(1,SC_NS);
147         flag_to_scanner_3->write(0);wait(1,SC_NS);
148     #endif
149     #if PAR > 4
150         flag_to_scanner_4->write(0);wait(1,SC_NS);
151         flag_to_scanner_5->write(0);wait(1,SC_NS);
152         flag_to_scanner_6->write(0);wait(1,SC_NS);
153         flag_to_scanner_7->write(0);wait(1,SC_NS);
154     #endif
155 }
156
157
158
159
160 //postprocessing after result received
161
162
163 hashes_done=scan_work.hashes_done;
164 //printf("from miner_thread: hash done %d\n",hashes_done);
165
166
167 /* record scanhash elapsed time */
168 gettimeofday(&tv_end, NULL);
169 timeval_subtract(&diff, &tv_end, &tv_start);
170 if (diff.tv_usec || diff.tv_sec) {
171
172     thr_hashrate = hashes_done / (diff.tv_sec + 1e-6 * diff.tv_usec);
173
174 }
175
176 if (OPT_BENCHMARK) {
177     double hashrate = 0;
178     hashrate += thr_hashrate;
179     sprintf(s, hashrate >= 1e6 ? "%.0f" : "%.2f", 1e-3 * hashrate*N);
180     printf( "Total:_%s_khash/s\n\n\n", s);
181 }
182
183
184
185
186 for(int j=0;j<32;j++)
187     g_work.data[j]=scan_work.data[j];
188
189 WorkOut->write(g_work);wait(1,SC_NS);
190

```

```

191     }
192
193
194 out:
195
196
197     return;
198 }
199
200 void Miner_Thread::main()
201 {
202
203     miner_thread();
204 }

```

Listing 9: platform.h

```

1  #ifndef _PLATFORM
2  #define _PLATFORM
3
4  #include "systemc.h"
5  #include "types.h"
6
7  #include "getwork.h"
8  #include "submitwork.h"
9  #include "miner.h"
10
11 class Platform : public sc_module
12 {
13     //get work from outside
14     //proceed by miner_thread
15     //send to scan_hash
16     //back to miner_thread
17     //send to outside
18 public:
19
20     c_work_queue Work_q1;
21     c_work_queue Work_q2;
22
23
24     /*
25     miner_thread send work to scan_hash
26     scan_hash hashes and send back hash to miner_thread
27     */
28     Miner miner;
29     Get_Work get_work;
30     Submit_Work submit_work;
31
32
33
34
35     SC_HAS_PROCESS(Platform);
36
37     //CONSTRUCTOR
38     Platform(sc_module_name name);
39
40     //METHODS
41 };
42
43 #endif

```

Listing 10: platform.cc

```

1  #include "platform.h"
2
3  Platform::Platform(sc_module_name name)
4  : sc_module(name)
5  , miner("miner")

```

```

6 , get_work("get_work")
7 , submit_work("submit_work")
8 , Work_q1("Work_q1", BUF_SIZE1)
9 , Work_q2("Work_q2", BUF_SIZE1)
10 {
11     get_work.WorkOut(Work_q1);
12     miner.WorkIn(Work_q1);
13
14     miner.WorkOut(Work_q2);
15     submit_work.WorkIn(Work_q2);
16 }

```

Listing 11: scan_hash.h

```

1 #ifndef _Scan_Hash
2 #define _Scan_Hash
3
4
5 #include "systemc.h"
6 #include "types.h"
7
8 class Scan_Hash : public sc_module
9 {
10
11 public:
12
13     sc_port<i_num_sender> flag_to_main;
14     sc_port<i_scan_work_sender> work_to_main;
15
16     sc_port<i_num_receiver> flag_from_main;
17     sc_port<i_scan_work_receiver> work_from_main;
18
19     sc_port<i_config_scan_work_receiver> config_from_main;
20     int flag;
21
22
23     struct scan_work scan_work;
24     struct config_scan_work config_work;
25
26     //CONSTRUCTOR
27     SC_HAS_PROCESS(Scan_Hash);
28     Scan_Hash(sc_module_name name);
29
30     //METHODS
31     void main();
32 };
33
34
35 #endif

```

Listing 12: scan_hash.cc

```

1 #include "scan_hash.h"
2 #include "sha2.h"
3 #include "util.h"
4
5 Scan_Hash::Scan_Hash(sc_module_name name)
6 : sc_module(name)
7 {
8     SC_THREAD(main);
9     SET_STACK_SIZE();
10 }
11
12
13
14
15 void Scan_Hash::main()
16 {

```

```

17
18     bool rc;
19
20     uint32_t data[64];
21     uint32_t hash[8];
22     uint32_t midstate[8];
23     uint32_t prehash[8];
24     uint32_t n;
25     uint32_t first_nonce;
26     uint32_t Htarg;
27     int loop_id;
28     int index;
29
30     while(true){
31
32         flag_from_main->read(flag);
33         if(flag>0){
34
35
36             work_from_main->read(scan_work);
37             config_from_main->read(config_work);
38             index=config_work.index;
39             n = scan_work.data[19]+config_work.start_point - 1;
40             first_nonce = scan_work.data[19]+config_work.start_point;
41             Htarg = scan_work.target[7];
42
43             memcpy(data, scan_work.data + 16, 64);
44             sha256d_preextend(data);
45
46             sha256_init(midstate);
47             sha256_transform(midstate, scan_work.data, 0);
48             memcpy(prehash, midstate, 32);
49             sha256d_prehash(prehash, scan_work.data + 16);
50             loop_id=0;
51             printf("hashing_block:");
52             printf("_%d_",index);
53             /*for(int m=0;m<8;m++)
54                 printf("%08x",scan_work.target[m]);*/
55             printf("nonce_starting_from_%ld\n",first_nonce);
56
57         }
58
59         data[3] = ++n;
60         sha256d_ms(hash, data, midstate, prehash);
61         if (swab32(hash[7]) <= Htarg) {
62             scan_work.data[19] = data[3];
63             sha256d_80_swap(hash, scan_work.data);
64             if (fulltest(hash, scan_work.target)) {
65                 flag_to_main->write(1);
66
67                 scan_work.hashes_done = n - first_nonce + 1;
68                 scan_work.data[19] = n;
69
70
71
72                 printf("\nhash_found_from_%d\nsha2_n=%ld\nhash_done=%d\nhash:_%n",index,n,scan_work.
73                     hashes_done);
74                 for(int m=0;m<8;m++)
75                     printf("%08x",hash[m]);
76                 printf("\n");
77
78                 work_to_main->write(scan_work);
79                 continue;
80
81             }
82
83         }
84     }
85     else{

```



```

86         flag_to_main->write(0);
87     }
88     /*
89     if((loop_id%10000)==0){
90
91         printf("\nloop= %ld n= %ld index %d\nhash  : \n",loop_id,n,index);
92         for(int m=0;m<8;m++)
93             printf("%08x",hash[m]);
94
95
96     }*/
97
98     loop_id++;
99     //wait(1,SC_NS);
100
101 }
102
103
104     return ;
105
106 }

```

Listing 13: sha2.h

```

1  #ifndef _SHA2
2  #define _SHA2
3
4  #include "types.h"
5
6
7  const uint32_t sha256d_hash1[16] = {
8      0x00000000, 0x00000000, 0x00000000, 0x00000000,
9      0x00000000, 0x00000000, 0x00000000, 0x00000000,
10     0x80000000, 0x00000000, 0x00000000, 0x00000000,
11     0x00000000, 0x00000000, 0x00000000, 0x00000100
12 };
13
14 //H(n)=H(n-1)+C(H(n-1)), C is the compress
15 const uint32_t sha256_h[8] = { //each is 32 bits, total 256 bits
16     0x6a09e667, 0xbb67ae85, 0x3c6ef372, 0xa54ff53a,
17     0x510e527f, 0x9b05688c, 0x1f83d9ab, 0x5be0cd19
18 };
19
20
21 //T1=h + ?l(e) + C h(e; f ; g ) + Kj+ Wj
22 const uint32_t sha256_k[64] = {
23     0x428a2f98, 0x71374491, 0xb5c0fbcf, 0xe9b5dba5,
24     0x3956c25b, 0x59f111f1, 0x923f82a4, 0xab1c5ed5,
25     0xd807aa98, 0x12835b01, 0x243185be, 0x550c7dc3,
26     0x72be5d74, 0x80deb1fe, 0x9bdc06a7, 0xc19bf174,
27     0xe49b69c1, 0xefbe4786, 0x0fc19dc6, 0x240ca1cc,
28     0x2de92c6f, 0x4a7484aa, 0x5cb0a9dc, 0x76f988da,
29     0x983e5152, 0xa831c66d, 0xb00327c8, 0xbf597fc7,
30     0xc6e00bf3, 0xd5a79147, 0x06ca6351, 0x14292967,
31     0x27b70a85, 0x2e1b2138, 0x4d2c6dfc, 0x53380d13,
32     0x650a7354, 0x766a0abb, 0x81c2c92e, 0x92722c85,
33     0xa2bfe8a1, 0xa81a664b, 0xc24b8b70, 0xc76c51a3,
34     0xd192e819, 0xd6990624, 0xf40e3585, 0x106aa070,
35     0x19a4c116, 0x1e376c08, 0x2748774c, 0x34b0bcb5,
36     0x391c0cb3, 0x4ed8aa4a, 0x5b9cca4f, 0x682e6ff3,
37     0x748f82ee, 0x78a5636f, 0x84c87814, 0x8cc70208,
38     0x90befffa, 0xa4506ceb, 0xbef9a3f7, 0xc67178f2
39 };
40
41
42
43
44
45

```

```

46 #define RND(a, b, c, d, e, f, g, h, k) \
47     do { \
48         t0 = h + S1(e) + Ch(e, f, g) + k; \
49         t1 = S0(a) + Maj(a, b, c); \
50         d += t0; \
51         h = t0 + t1; \
52     } while (0)
53
54
55 #define RNDr(S, W, i) \
56     RND(S[(64 - i) % 8], S[(65 - i) % 8], \
57         S[(66 - i) % 8], S[(67 - i) % 8], \
58         S[(68 - i) % 8], S[(69 - i) % 8], \
59         S[(70 - i) % 8], S[(71 - i) % 8], \
60         W[i] + sha256_k[i])
61
62
63
64 void sha256d_prehash(uint32_t *S, const uint32_t *W);
65 void sha256d_ms(uint32_t *hash, uint32_t *W,
66     const uint32_t *midstate, const uint32_t *prehash);
67 void sha256d_80_swap(uint32_t *hash, const uint32_t *data);
68 void sha256d_preextend(uint32_t *W);
69 void sha256_init(uint32_t *state);
70 void sha256_transform(uint32_t *state, const uint32_t *block, int swap);
71
72
73
74 #endif

```

Listing 14: sha2.cc

```

1 #include "sha2.h"
2 #include "util.h"
3
4 /* Elementary functions used by SHA256 */
5 #define Ch(x, y, z) ((x & (y ^ z)) ^ z)
6 #define Maj(x, y, z) ((x & (y | z)) | (y & z))
7 #define ROTR(x, n) ((x >> n) | (x << (32 - n)))
8 #define S0(x) (ROTR(x, 2) ^ ROTR(x, 13) ^ ROTR(x, 22))
9 #define S1(x) (ROTR(x, 6) ^ ROTR(x, 11) ^ ROTR(x, 25))
10 #define s0(x) (ROTR(x, 7) ^ ROTR(x, 18) ^ (x >> 3))
11 #define s1(x) (ROTR(x, 17) ^ ROTR(x, 19) ^ (x >> 10))
12
13
14
15 void sha256d_prehash(uint32_t *S, const uint32_t *W)
16 {
17     uint32_t t0, t1;
18     RNDr(S, W, 0);
19     RNDr(S, W, 1);
20     RNDr(S, W, 2);
21 }
22
23
24 void sha256d_ms(uint32_t *hash, uint32_t *W,
25     const uint32_t *midstate, const uint32_t *prehash)
26 {
27     uint32_t S[64];
28     uint32_t t0, t1;
29     int i;
30
31     S[18] = W[18];
32     S[19] = W[19];
33     S[20] = W[20];
34     S[22] = W[22];
35     S[23] = W[23];
36     S[24] = W[24];
37     S[30] = W[30];

```

```

38     S[31] = W[31];
39
40     W[18] += s0(W[3]);
41     W[19] += W[3];
42     W[20] += s1(W[18]);
43     W[21] = s1(W[19]);
44     W[22] += s1(W[20]);
45     W[23] += s1(W[21]);
46     W[24] += s1(W[22]);
47     W[25] = s1(W[23]) + W[18];
48     W[26] = s1(W[24]) + W[19];
49     W[27] = s1(W[25]) + W[20];
50     W[28] = s1(W[26]) + W[21];
51     W[29] = s1(W[27]) + W[22];
52     W[30] += s1(W[28]) + W[23];
53     W[31] += s1(W[29]) + W[24];
54     for (i = 32; i < 64; i += 2) {
55         W[i] = s1(W[i - 2]) + W[i - 7] + s0(W[i - 15]) + W[i - 16];
56         W[i+1] = s1(W[i - 1]) + W[i - 6] + s0(W[i - 14]) + W[i - 15];
57     }
58
59     memcpy(S, prehash, 32);
60
61     RNDr(S, W, 3);
62     RNDr(S, W, 4);
63     RNDr(S, W, 5);
64     RNDr(S, W, 6);
65     RNDr(S, W, 7);
66     RNDr(S, W, 8);
67     RNDr(S, W, 9);
68     RNDr(S, W, 10);
69     RNDr(S, W, 11);
70     RNDr(S, W, 12);
71     RNDr(S, W, 13);
72     RNDr(S, W, 14);
73     RNDr(S, W, 15);
74     RNDr(S, W, 16);
75     RNDr(S, W, 17);
76     RNDr(S, W, 18);
77     RNDr(S, W, 19);
78     RNDr(S, W, 20);
79     RNDr(S, W, 21);
80     RNDr(S, W, 22);
81     RNDr(S, W, 23);
82     RNDr(S, W, 24);
83     RNDr(S, W, 25);
84     RNDr(S, W, 26);
85     RNDr(S, W, 27);
86     RNDr(S, W, 28);
87     RNDr(S, W, 29);
88     RNDr(S, W, 30);
89     RNDr(S, W, 31);
90     RNDr(S, W, 32);
91     RNDr(S, W, 33);
92     RNDr(S, W, 34);
93     RNDr(S, W, 35);
94     RNDr(S, W, 36);
95     RNDr(S, W, 37);
96     RNDr(S, W, 38);
97     RNDr(S, W, 39);
98     RNDr(S, W, 40);
99     RNDr(S, W, 41);
100    RNDr(S, W, 42);
101    RNDr(S, W, 43);
102    RNDr(S, W, 44);
103    RNDr(S, W, 45);
104    RNDr(S, W, 46);
105    RNDr(S, W, 47);
106    RNDr(S, W, 48);
107    RNDr(S, W, 49);

```

```

108     RNDr(S, W, 50);
109     RNDr(S, W, 51);
110     RNDr(S, W, 52);
111     RNDr(S, W, 53);
112     RNDr(S, W, 54);
113     RNDr(S, W, 55);
114     RNDr(S, W, 56);
115     RNDr(S, W, 57);
116     RNDr(S, W, 58);
117     RNDr(S, W, 59);
118     RNDr(S, W, 60);
119     RNDr(S, W, 61);
120     RNDr(S, W, 62);
121     RNDr(S, W, 63);
122
123     for (i = 0; i < 8; i++)
124         S[i] += midstate[i];
125
126     W[18] = S[18];
127     W[19] = S[19];
128     W[20] = S[20];
129     W[22] = S[22];
130     W[23] = S[23];
131     W[24] = S[24];
132     W[30] = S[30];
133     W[31] = S[31];
134
135     memcpy(S + 8, sha256d_hash1 + 8, 32);
136     S[16] = s1(sha256d_hash1[14]) + sha256d_hash1[ 9] + s0(S[ 1]) + S[ 0];
137     S[17] = s1(sha256d_hash1[15]) + sha256d_hash1[10] + s0(S[ 2]) + S[ 1];
138     S[18] = s1(S[16]) + sha256d_hash1[11] + s0(S[ 3]) + S[ 2];
139     S[19] = s1(S[17]) + sha256d_hash1[12] + s0(S[ 4]) + S[ 3];
140     S[20] = s1(S[18]) + sha256d_hash1[13] + s0(S[ 5]) + S[ 4];
141     S[21] = s1(S[19]) + sha256d_hash1[14] + s0(S[ 6]) + S[ 5];
142     S[22] = s1(S[20]) + sha256d_hash1[15] + s0(S[ 7]) + S[ 6];
143     S[23] = s1(S[21]) + S[16] + s0(sha256d_hash1[ 8]) + S[ 7];
144     S[24] = s1(S[22]) + S[17] + s0(sha256d_hash1[ 9]) + sha256d_hash1[ 8];
145     S[25] = s1(S[23]) + S[18] + s0(sha256d_hash1[10]) + sha256d_hash1[ 9];
146     S[26] = s1(S[24]) + S[19] + s0(sha256d_hash1[11]) + sha256d_hash1[10];
147     S[27] = s1(S[25]) + S[20] + s0(sha256d_hash1[12]) + sha256d_hash1[11];
148     S[28] = s1(S[26]) + S[21] + s0(sha256d_hash1[13]) + sha256d_hash1[12];
149     S[29] = s1(S[27]) + S[22] + s0(sha256d_hash1[14]) + sha256d_hash1[13];
150     S[30] = s1(S[28]) + S[23] + s0(sha256d_hash1[15]) + sha256d_hash1[14];
151     S[31] = s1(S[29]) + S[24] + s0(S[16]) + sha256d_hash1[15];
152     for (i = 32; i < 60; i += 2) {
153         S[i] = s1(S[i - 2]) + S[i - 7] + s0(S[i - 15]) + S[i - 16];
154         S[i+1] = s1(S[i - 1]) + S[i - 6] + s0(S[i - 14]) + S[i - 15];
155     }
156     S[60] = s1(S[58]) + S[53] + s0(S[45]) + S[44];
157
158     sha256_init(hash);
159
160     RNDr(hash, S, 0);
161     RNDr(hash, S, 1);
162     RNDr(hash, S, 2);
163     RNDr(hash, S, 3);
164     RNDr(hash, S, 4);
165     RNDr(hash, S, 5);
166     RNDr(hash, S, 6);
167     RNDr(hash, S, 7);
168     RNDr(hash, S, 8);
169     RNDr(hash, S, 9);
170     RNDr(hash, S, 10);
171     RNDr(hash, S, 11);
172     RNDr(hash, S, 12);
173     RNDr(hash, S, 13);
174     RNDr(hash, S, 14);
175     RNDr(hash, S, 15);
176     RNDr(hash, S, 16);
177     RNDr(hash, S, 17);

```

```

178     RNDr(hash, S, 18);
179     RNDr(hash, S, 19);
180     RNDr(hash, S, 20);
181     RNDr(hash, S, 21);
182     RNDr(hash, S, 22);
183     RNDr(hash, S, 23);
184     RNDr(hash, S, 24);
185     RNDr(hash, S, 25);
186     RNDr(hash, S, 26);
187     RNDr(hash, S, 27);
188     RNDr(hash, S, 28);
189     RNDr(hash, S, 29);
190     RNDr(hash, S, 30);
191     RNDr(hash, S, 31);
192     RNDr(hash, S, 32);
193     RNDr(hash, S, 33);
194     RNDr(hash, S, 34);
195     RNDr(hash, S, 35);
196     RNDr(hash, S, 36);
197     RNDr(hash, S, 37);
198     RNDr(hash, S, 38);
199     RNDr(hash, S, 39);
200     RNDr(hash, S, 40);
201     RNDr(hash, S, 41);
202     RNDr(hash, S, 42);
203     RNDr(hash, S, 43);
204     RNDr(hash, S, 44);
205     RNDr(hash, S, 45);
206     RNDr(hash, S, 46);
207     RNDr(hash, S, 47);
208     RNDr(hash, S, 48);
209     RNDr(hash, S, 49);
210     RNDr(hash, S, 50);
211     RNDr(hash, S, 51);
212     RNDr(hash, S, 52);
213     RNDr(hash, S, 53);
214     RNDr(hash, S, 54);
215     RNDr(hash, S, 55);
216     RNDr(hash, S, 56);
217
218     hash[2] += hash[6] + S1(hash[3]) + Ch(hash[3], hash[4], hash[5])
219             + S[57] + sha256_k[57];
220     hash[1] += hash[5] + S1(hash[2]) + Ch(hash[2], hash[3], hash[4])
221             + S[58] + sha256_k[58];
222     hash[0] += hash[4] + S1(hash[1]) + Ch(hash[1], hash[2], hash[3])
223             + S[59] + sha256_k[59];
224     hash[7] += hash[3] + S1(hash[0]) + Ch(hash[0], hash[1], hash[2])
225             + S[60] + sha256_k[60]
226             + sha256_h[7];
227 }
228
229
230 void sha256d_80_swap(uint32_t *hash, const uint32_t *data)
231 {
232     uint32_t S[16];
233     int i;
234
235     //first sha256, performed to data, stored into S
236     sha256_init(S);
237     //data is 1024 bits, so to calculate the sha256, we need to perform the algo twice
238
239     sha256_transform(S, data, 0);
240     sha256_transform(S, data + 16, 0);
241     memcpy(S + 8, sha256d_hash1 + 8, 32);
242
243     //second sha256, performed to S, stored into hash
244     sha256_init(hash);
245     sha256_transform(hash, S, 0);
246     for (i = 0; i < 8; i++)
247         hash[i] = swab32(hash[i]);

```

```

248 }
249
250
251 void sha256d_preextend(uint32_t *W)
252 {
253     //W[i] = s1(W[i - 2]) + W[i - 7] + s0(W[i - 15]) + W[i - 16];
254     W[16] = s1(W[14]) + W[ 9] + s0(W[ 1]) + W[ 0];
255     W[17] = s1(W[15]) + W[10] + s0(W[ 2]) + W[ 1];
256     W[18] = s1(W[16]) + W[11]           + W[ 2];
257     W[19] = s1(W[17]) + W[12] + s0(W[ 4]);
258     W[20] =           W[13] + s0(W[ 5]) + W[ 4];
259     W[21] =           W[14] + s0(W[ 6]) + W[ 5];
260     W[22] =           W[15] + s0(W[ 7]) + W[ 6];
261     W[23] =           W[16] + s0(W[ 8]) + W[ 7];
262     W[24] =           W[17] + s0(W[ 9]) + W[ 8];
263     W[25] =           s0(W[10]) + W[ 9];
264     W[26] =           s0(W[11]) + W[10];
265     W[27] =           s0(W[12]) + W[11];
266     W[28] =           s0(W[13]) + W[12];
267     W[29] =           s0(W[14]) + W[13];
268     W[30] =           s0(W[15]) + W[14];
269     W[31] =           s0(W[16]) + W[15];
270 }
271
272 void sha256_init(uint32_t *state)
273 {
274     memcpy(state, sha256_h, 32);
275 }
276
277
278 /*
279  * SHA256 block compression function. The 256-bit state is transformed via
280  * the 512-bit input block to produce a new state.
281  */
282 //this is the inner loop of the sha256 function
283 void sha256_transform(uint32_t *state, const uint32_t *block, int swap)
284 {
285     uint32_t W[64];
286     uint32_t S[8];
287     uint32_t t0, t1;
288     int i;
289
290     /* 1. Prepare message schedule W. */
291     if (swap) {
292         for (i = 0; i < 16; i++)
293             W[i] = swab32(block[i]);
294     } else
295         memcpy(W, block, 64);
296     //first initialize W
297     for (i = 16; i < 64; i += 2) {
298         W[i] = s1(W[i - 2]) + W[i - 7] + s0(W[i - 15]) + W[i - 16];
299         W[i+1] = s1(W[i - 1]) + W[i - 6] + s0(W[i - 14]) + W[i - 15];
300     }
301
302     /* 2. Initialize working variables. */
303     memcpy(S, state, 32); //S is now a=h1,b=h2,c=h3,...
304
305     /* 3. Mix. */
306     //64 loops
307     RNDr(S, W, 0);
308     RNDr(S, W, 1);
309     RNDr(S, W, 2);
310     RNDr(S, W, 3);
311     RNDr(S, W, 4);
312     RNDr(S, W, 5);
313     RNDr(S, W, 6);
314     RNDr(S, W, 7);
315     RNDr(S, W, 8);
316     RNDr(S, W, 9);
317     RNDr(S, W, 10);

```

```

318     RNDr(S, W, 11);
319     RNDr(S, W, 12);
320     RNDr(S, W, 13);
321     RNDr(S, W, 14);
322     RNDr(S, W, 15);
323     RNDr(S, W, 16);
324     RNDr(S, W, 17);
325     RNDr(S, W, 18);
326     RNDr(S, W, 19);
327     RNDr(S, W, 20);
328     RNDr(S, W, 21);
329     RNDr(S, W, 22);
330     RNDr(S, W, 23);
331     RNDr(S, W, 24);
332     RNDr(S, W, 25);
333     RNDr(S, W, 26);
334     RNDr(S, W, 27);
335     RNDr(S, W, 28);
336     RNDr(S, W, 29);
337     RNDr(S, W, 30);
338     RNDr(S, W, 31);
339     RNDr(S, W, 32);
340     RNDr(S, W, 33);
341     RNDr(S, W, 34);
342     RNDr(S, W, 35);
343     RNDr(S, W, 36);
344     RNDr(S, W, 37);
345     RNDr(S, W, 38);
346     RNDr(S, W, 39);
347     RNDr(S, W, 40);
348     RNDr(S, W, 41);
349     RNDr(S, W, 42);
350     RNDr(S, W, 43);
351     RNDr(S, W, 44);
352     RNDr(S, W, 45);
353     RNDr(S, W, 46);
354     RNDr(S, W, 47);
355     RNDr(S, W, 48);
356     RNDr(S, W, 49);
357     RNDr(S, W, 50);
358     RNDr(S, W, 51);
359     RNDr(S, W, 52);
360     RNDr(S, W, 53);
361     RNDr(S, W, 54);
362     RNDr(S, W, 55);
363     RNDr(S, W, 56);
364     RNDr(S, W, 57);
365     RNDr(S, W, 58);
366     RNDr(S, W, 59);
367     RNDr(S, W, 60);
368     RNDr(S, W, 61);
369     RNDr(S, W, 62);
370     RNDr(S, W, 63);
371
372     /* 4. Mix local working variables into global state */
373     for (i = 0; i < 8; i++)
374         state[i] += S[i];
375 }

```

Listing 15: submitwork.h

```

1  #ifndef _SUBMIT_WORK
2  #define _SUBMIT_WORK
3
4  #include "systemc.h"
5  #include "types.h"
6
7  class Submit_Work : public sc_module
8  {

```

```

9
10 public:
11     sc_port<i_work_receiver> WorkIn;
12
13
14     struct work g_work; //this should be shared between many scanhash blocks
15
16     //CONSTRUCTOR
17     SC_HAS_PROCESS(Submit_Work);
18     Submit_Work(sc_module_name name);
19
20     //METHODS
21     bool workio_submit_work();
22
23
24     void main();
25 };
26
27
28 #endif

```

Listing 16: submitwork.cc

```

1 #include "submitwork.h"
2
3 Submit_Work::Submit_Work(sc_module_name name)
4 : sc_module(name)
5 {
6     SC_THREAD(main);
7     SET_STACK_SIZE();
8 }
9
10 bool Submit_Work::workio_submit_work()
11 {
12     int failures = 0;
13
14
15     return true;
16 }
17
18
19
20 void Submit_Work::main()
21 {
22     int n=1;
23     while(true){
24
25         WorkIn->read(g_work);
26
27         workio_submit_work();
28         n=n+1;
29     }
30 }

```

Listing 17: util.h

```

1 #ifndef _UTIL
2 #define _UTIL
3
4 #include "types.h"
5
6
7 bool fulltest(const uint32_t *hash, const uint32_t *target);
8 int timeval_subtract(struct timeval *result, struct timeval *x,
9     struct timeval *y);
10
11
12 #endif

```


Listing 18: util.cc

```

1  #include "util.h"
2
3  bool fulltest(const uint32_t *hash, const uint32_t *target)
4  {
5      int i;
6      bool rc = true;
7
8      for (i = 7; i >= 0; i--) {
9          if (hash[i] > target[i]) {
10             rc = false;
11             break;
12         }
13         if (hash[i] < target[i]) {
14             rc = true;
15             break;
16         }
17     }
18
19
20
21     return rc;
22 }
23
24
25 /* Subtract the 'struct timeval' values X and Y,
26    storing the result in RESULT.
27    Return 1 if the difference is negative, otherwise 0. */
28 int timeval_subtract(struct timeval *result, struct timeval *x,
29                     struct timeval *y)
30 {
31     /* Perform the carry for the later subtraction by updating Y. */
32     if (x->tv_usec < y->tv_usec) {
33         int nsec = (y->tv_usec - x->tv_usec) / 1000000 + 1;
34         y->tv_usec -= 1000000 * nsec;
35         y->tv_sec += nsec;
36     }
37     if (x->tv_usec - y->tv_usec > 1000000) {
38         int nsec = (x->tv_usec - y->tv_usec) / 1000000;
39         y->tv_usec += 1000000 * nsec;
40         y->tv_sec -= nsec;
41     }
42
43     /* Compute the time remaining to wait.
44      * 'tv_usec' is certainly positive. */
45     result->tv_sec = x->tv_sec - y->tv_sec;
46     result->tv_usec = x->tv_usec - y->tv_usec;
47
48     /* Return 1 if result is negative. */
49     return x->tv_sec < y->tv_sec;
50 }

```

A.2 Makefile

Listing 19: Makefile

```

1
2  SYSTEMC = /opt/pkg/systemc-2.3.1
3
4  INCLUDE = -I. -I$(SYSTEMC)/include
5  LIBRARY = $(SYSTEMC)/lib-linux64
6  CFLAG = $(INCLUDE) -L$(LIBRARY) -Xlinker -R -Xlinker $(LIBRARY) -lsystemc -O3
7
8  CC = g++
9  RM = rm -f
10

```

```
11 TARGETS = cpuminer
12 OFILES = Main.o \
13         util.o \
14         sha2.o \
15         getwork.o \
16         miner.o \
17         miner_thread.o \
18         platform.o \
19         scan_hash.o \
20         submitwork.o \
21
22
23
24 HFILES = types.h config.h
25
26 all: $(TARGETS)
27
28
29 clean:
30     $(RM) *.o $(TARGETS) *.gch
31
32
33 cpuminer: $(OFILES)
34     $(CC) $^ $(CFLAG) -o $@
35
36 %.o : %.cc $(HFILES)
37     $(CC) -c $^ $(INCLUDE) -O3
```